

Fig. 1

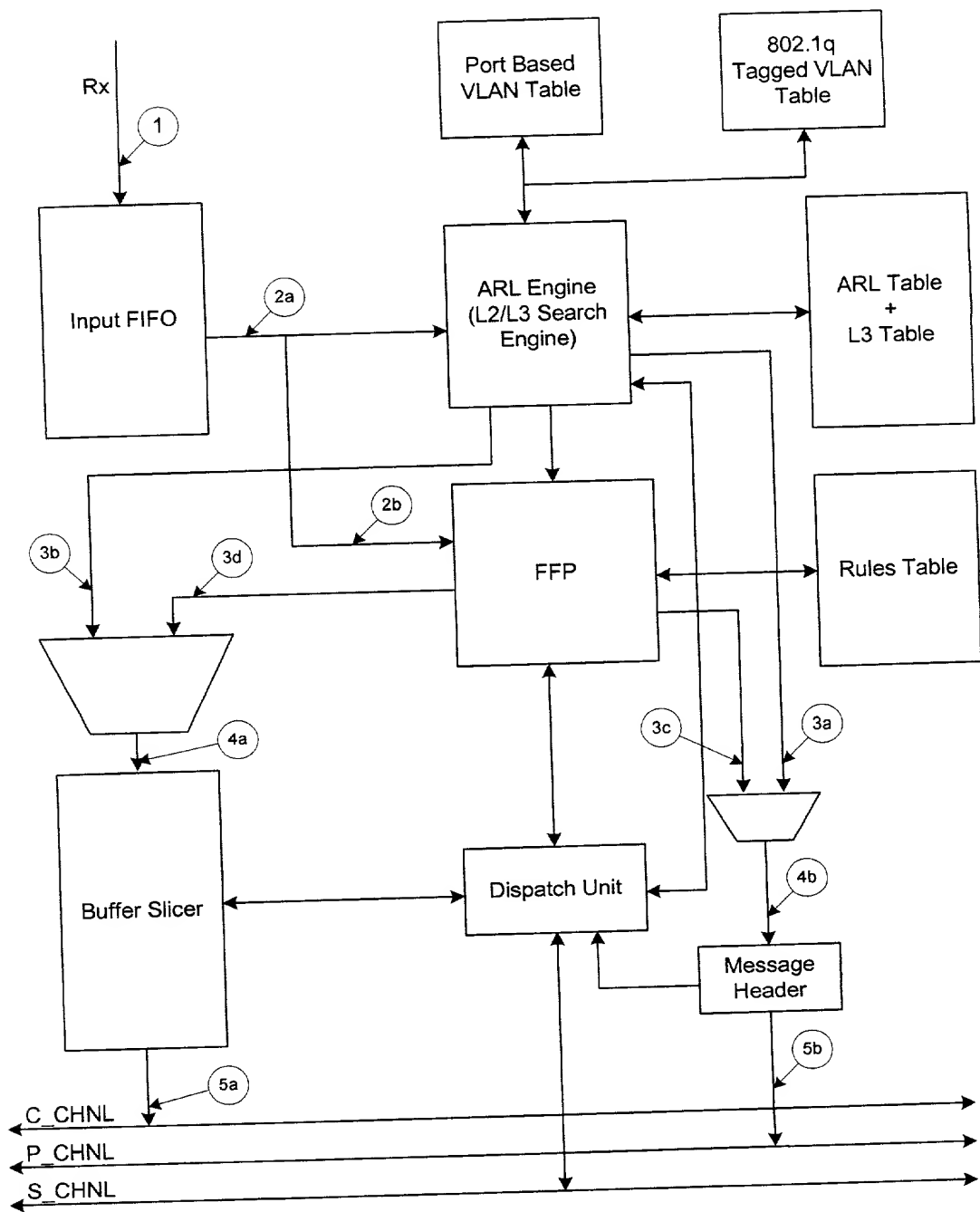


Fig. 2

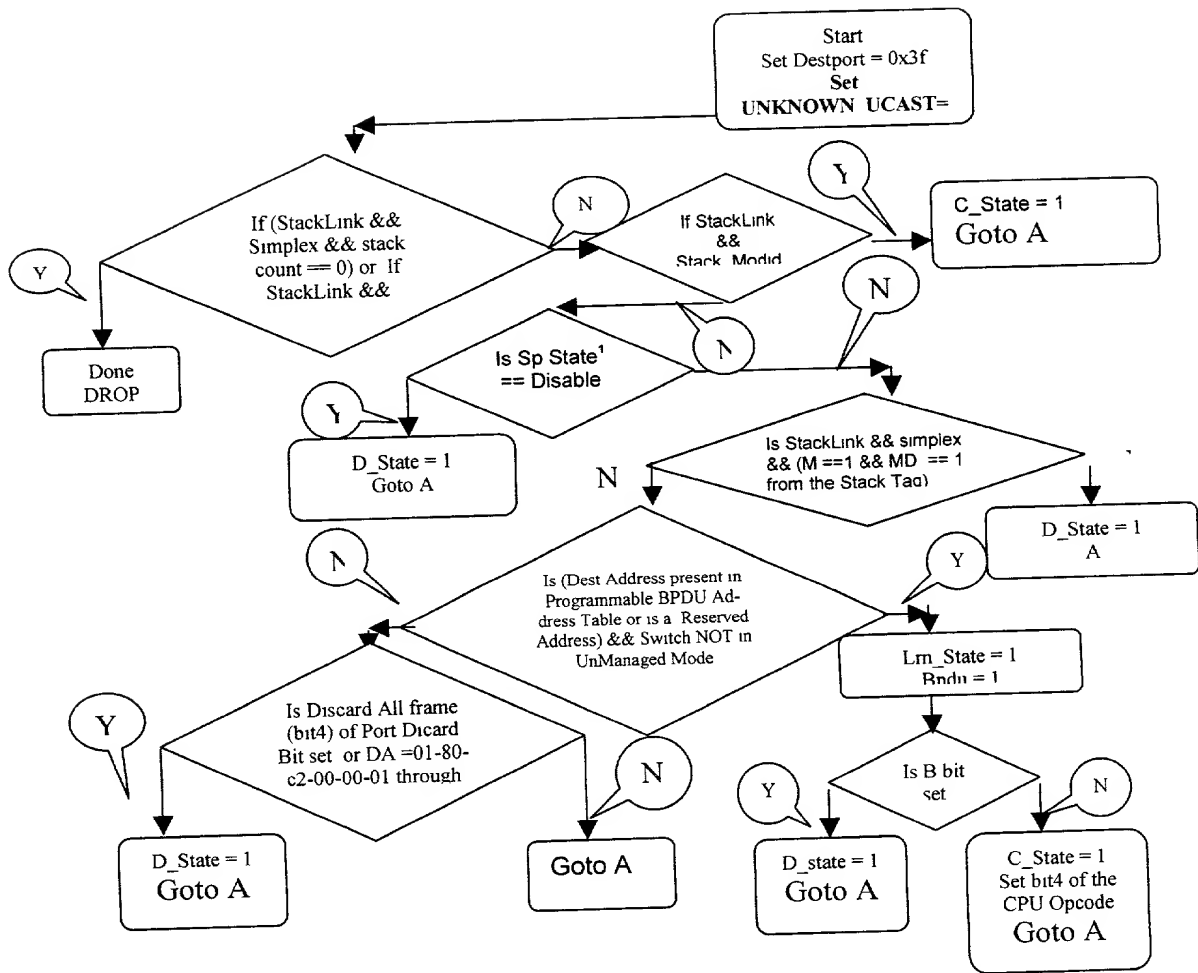


Fig. 3

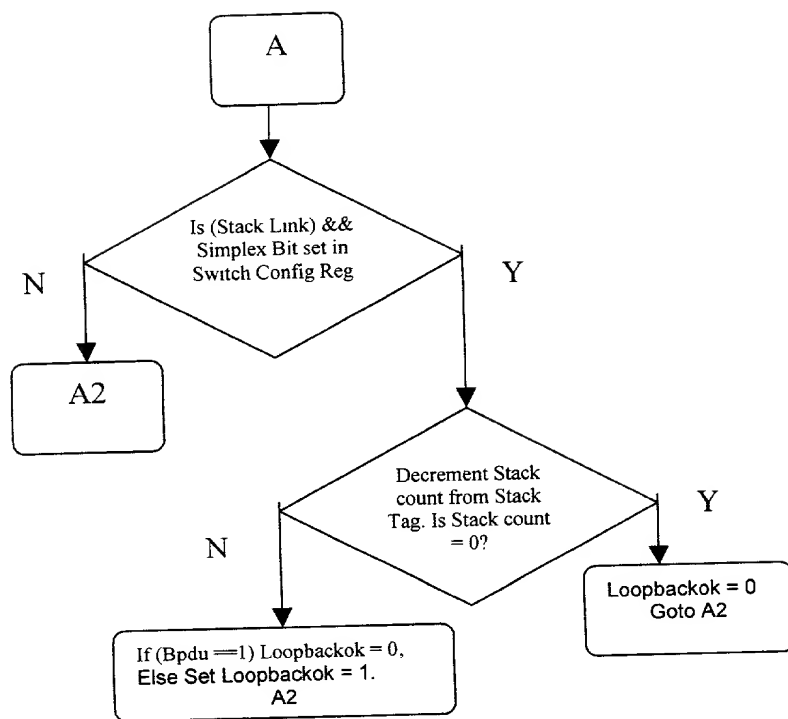


Fig. 4

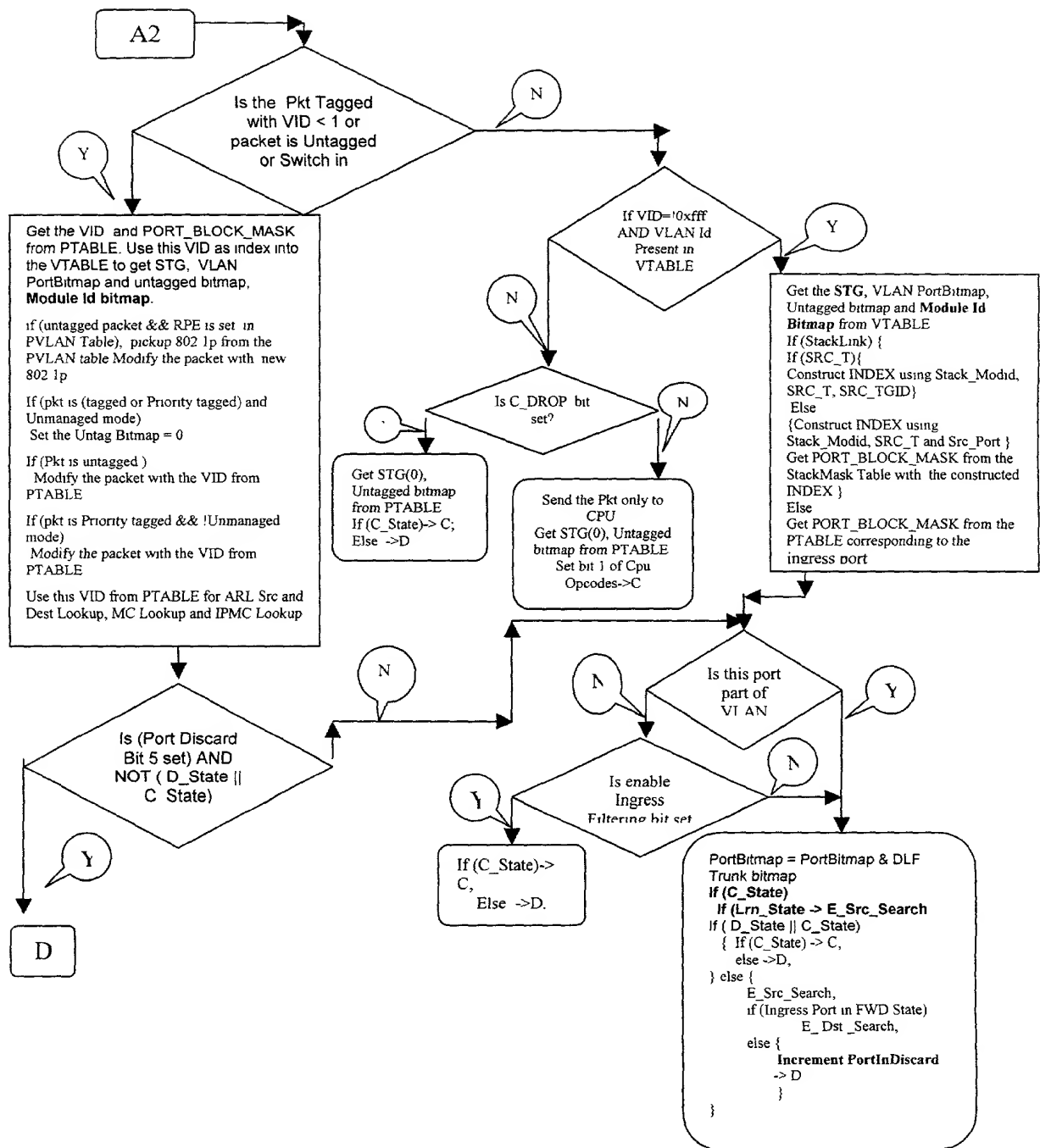


Fig. 5

607190-690/660

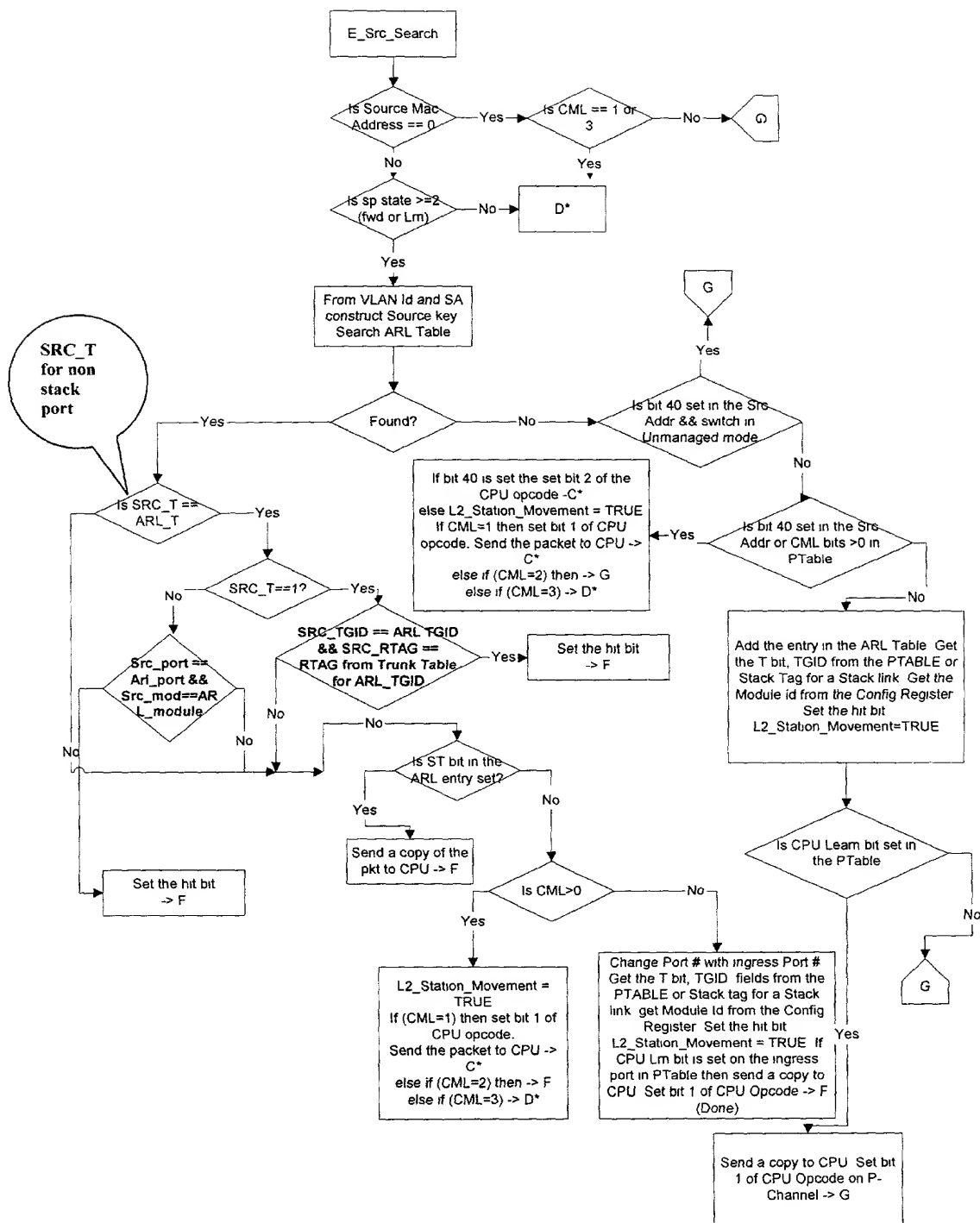


Fig. 6

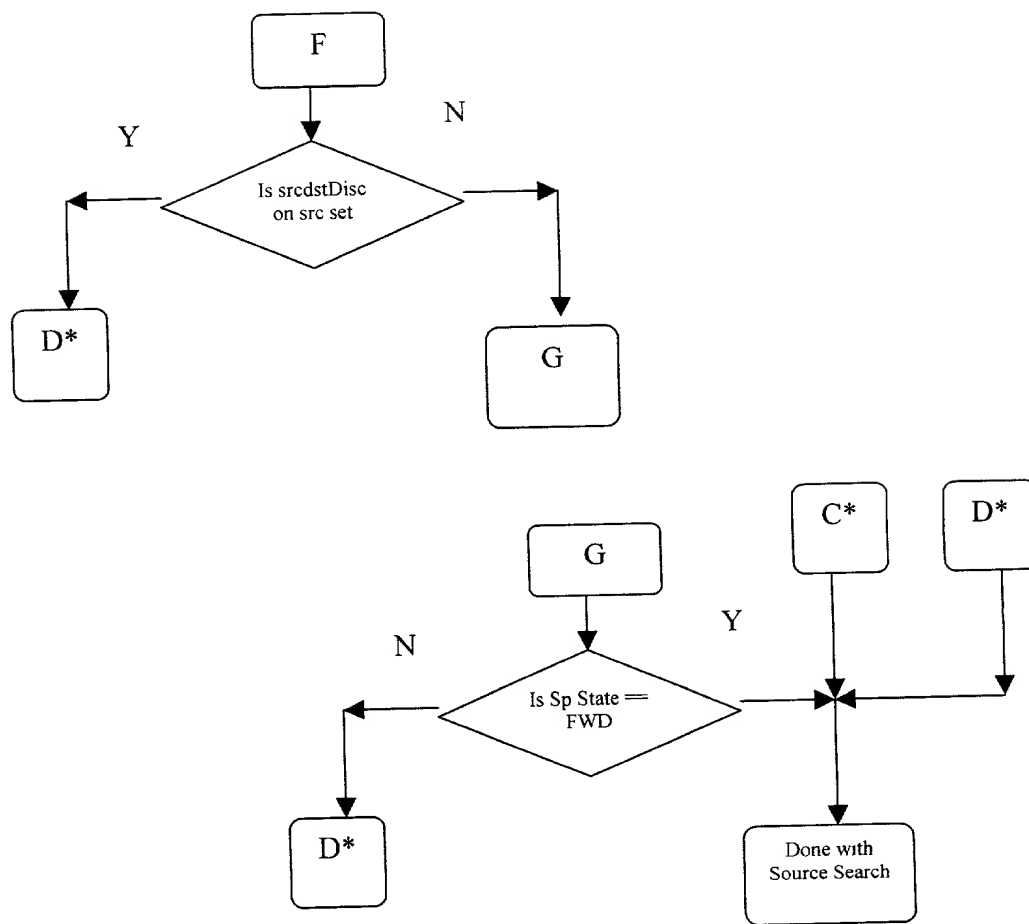


Fig. 7

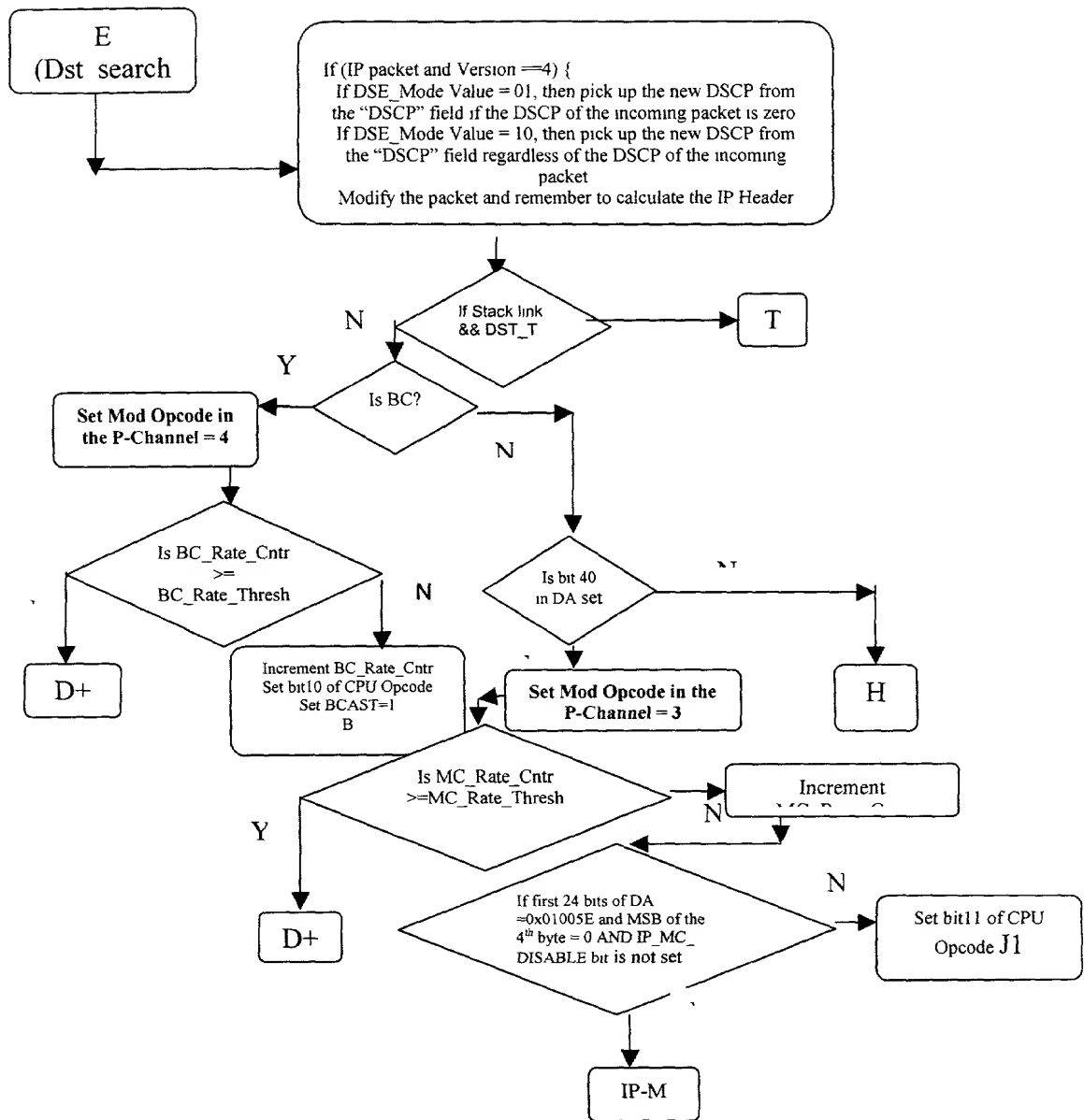


Fig. 8



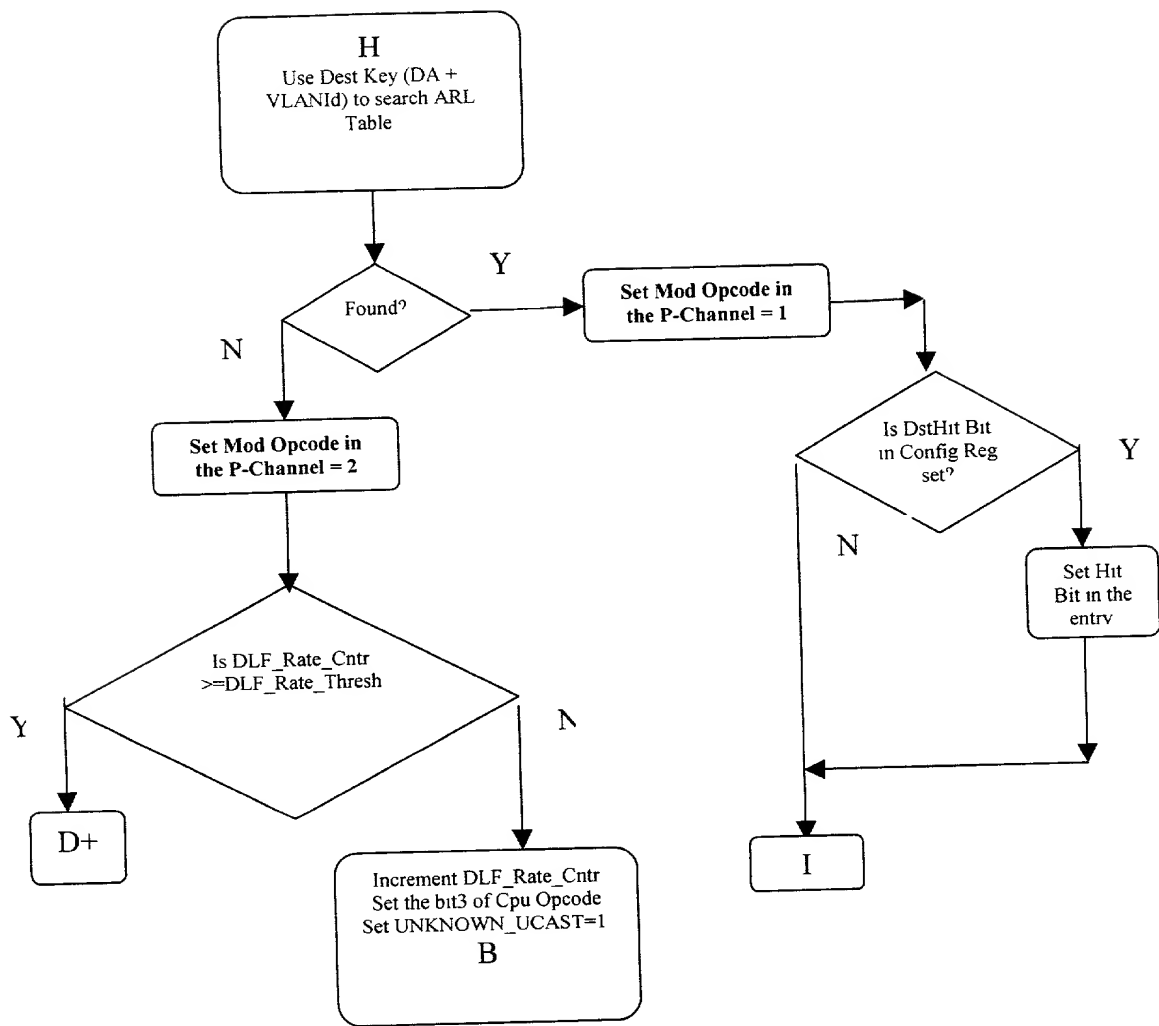


Fig. 9

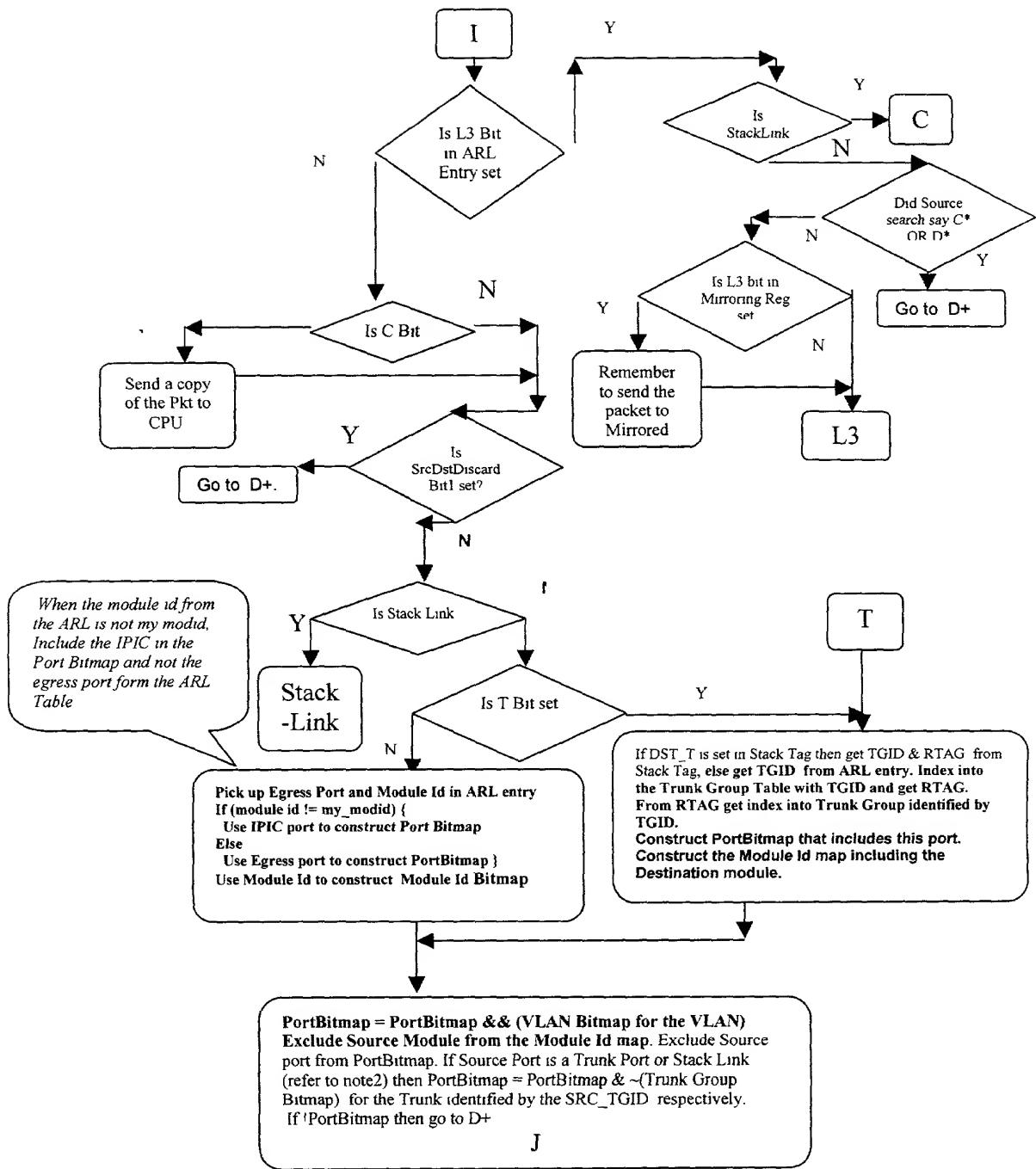


Fig. 10

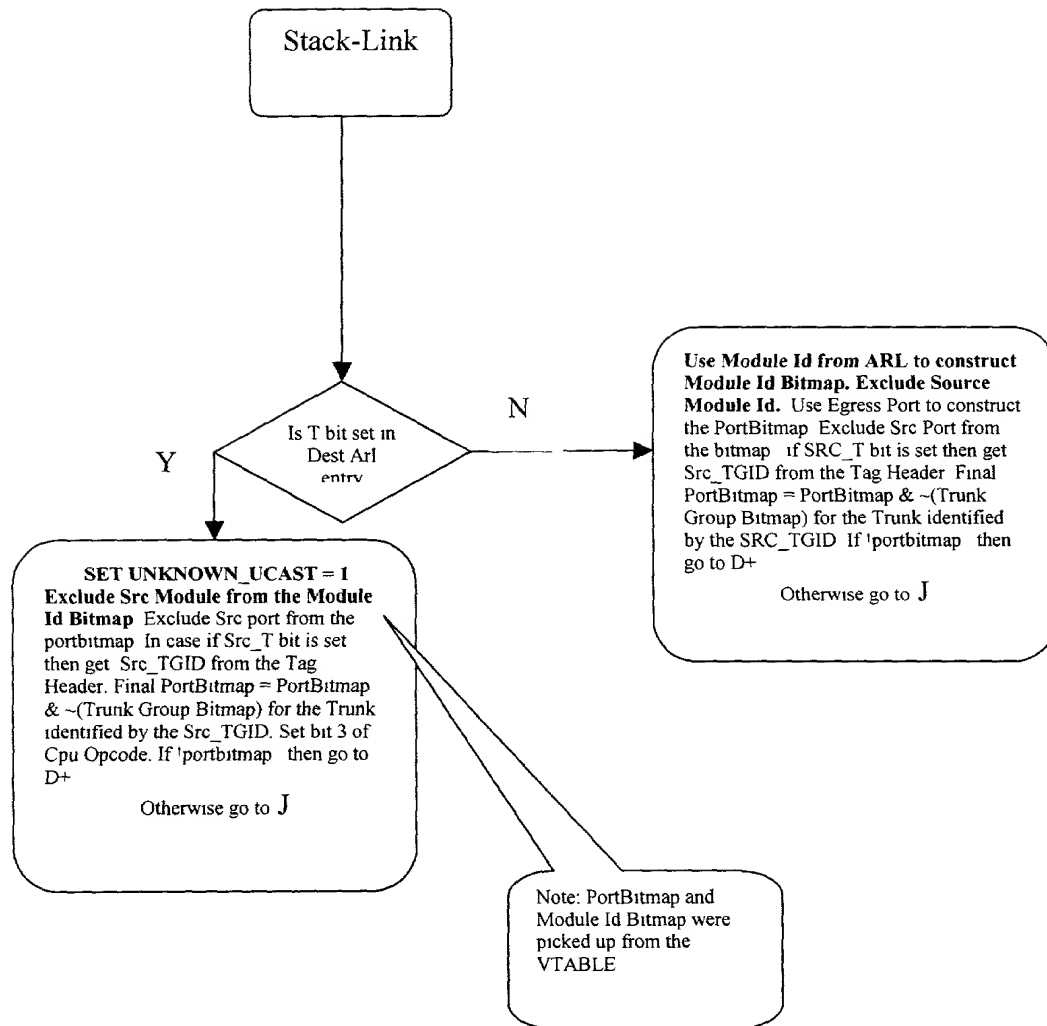


Fig. 11

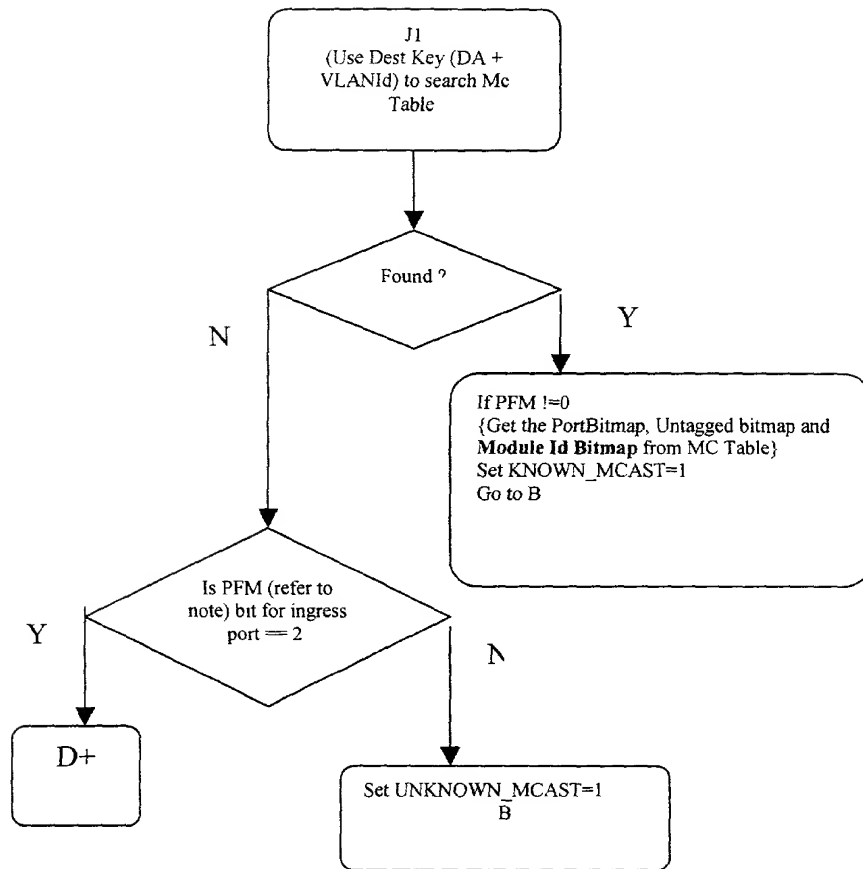


Fig. 12

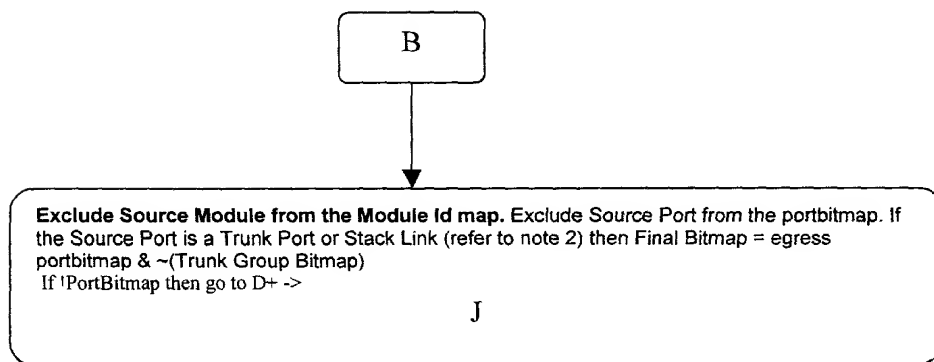


Fig. 13

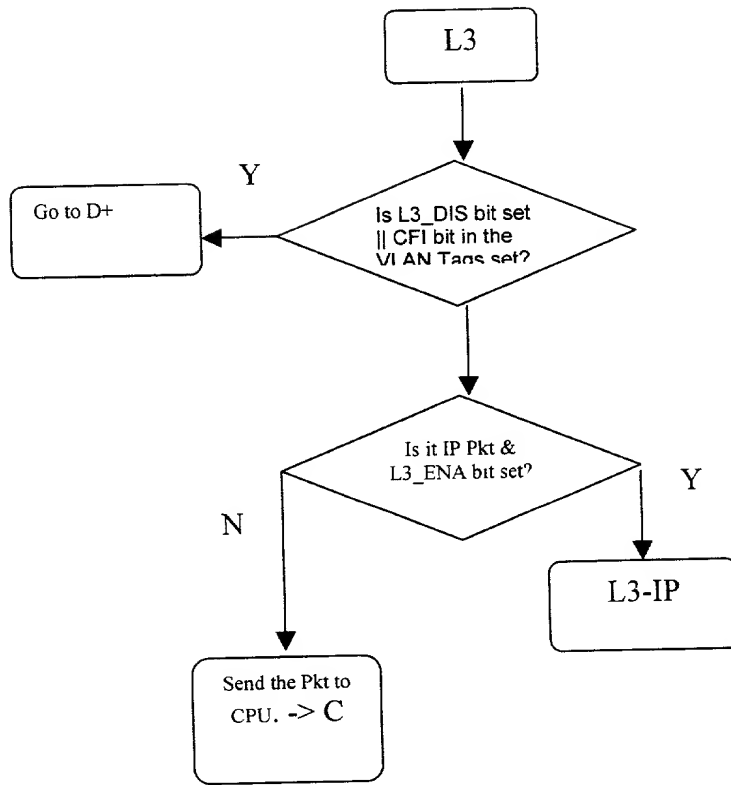


Fig. 14

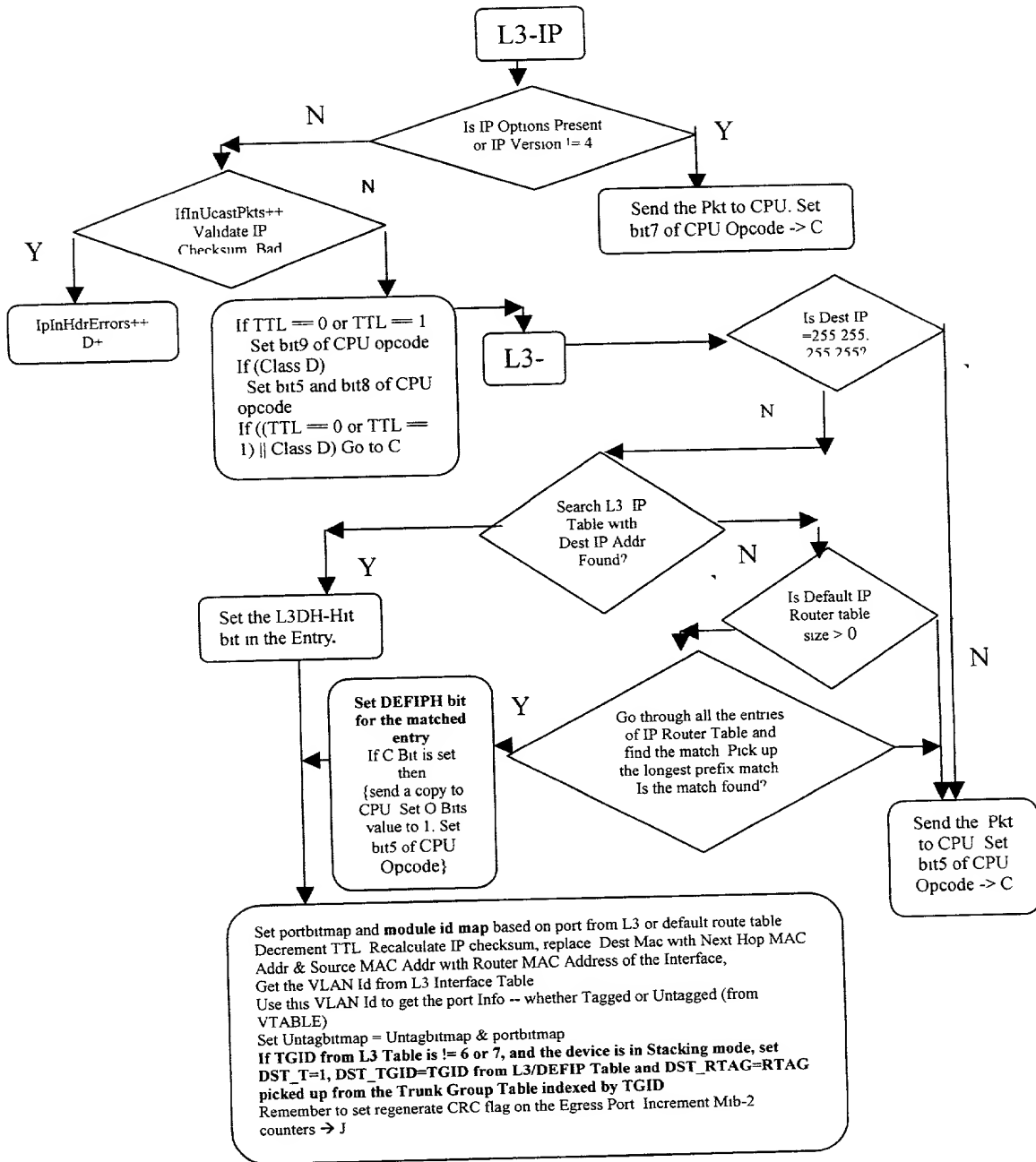


Fig. 15

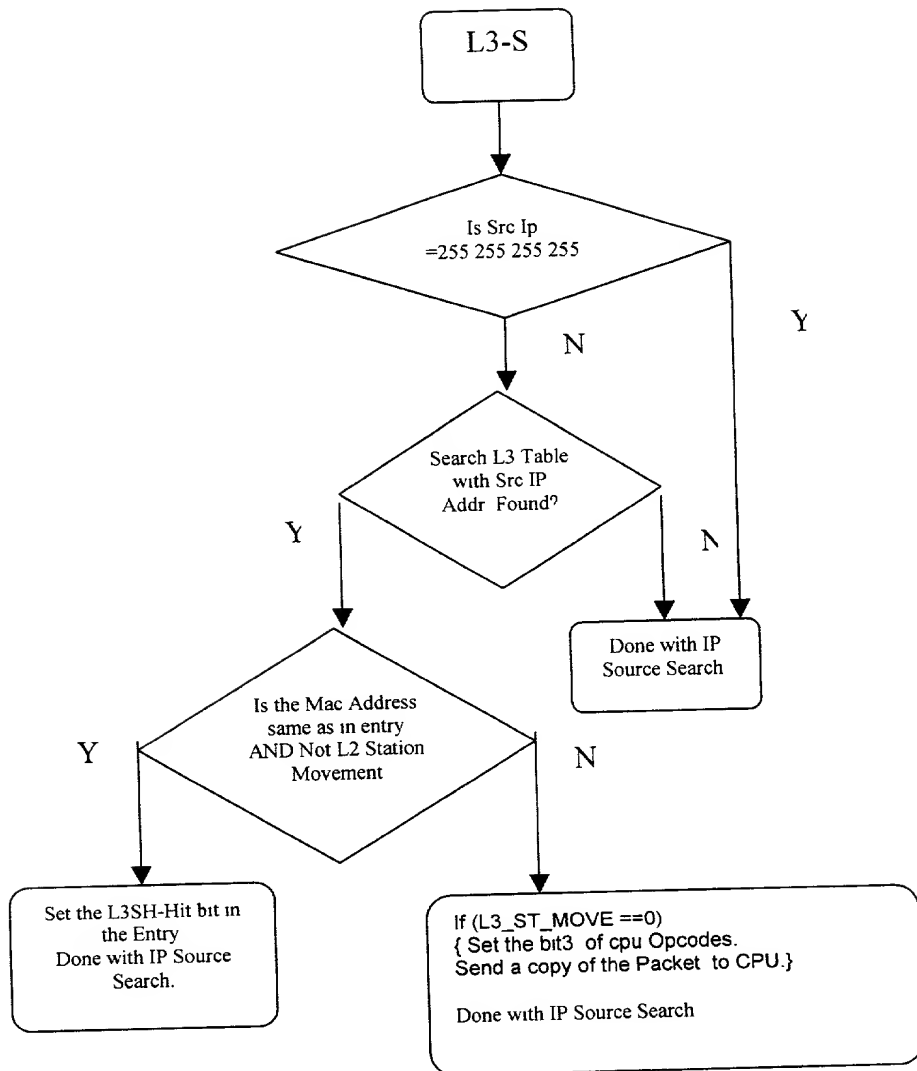


Fig. 16

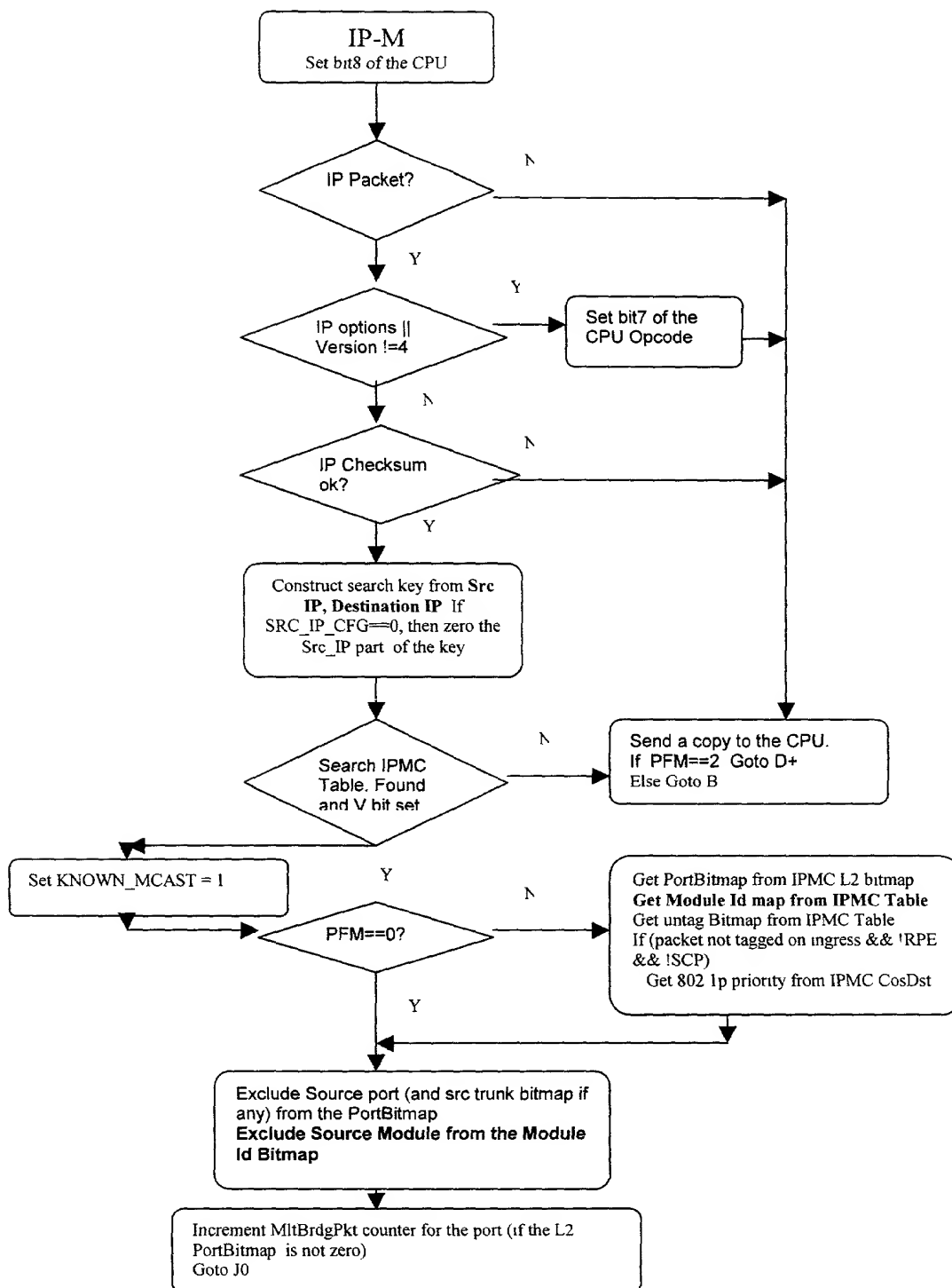


Fig. 17



```

graph TD
    J0([J0]) --> D1{Did Source search say C* or D*}
    D1 -- Y --> GotoJ1([Goto J])
    D1 -- N --> D2{L3 Mirror && Stack_link}
    D2 -- Y --> RememberMirror([Remember to Mirror])
    RememberMirror --> D3{Is L3_DIS bit set || CFI bit in the VLAN Tags set?}
    D2 -- N --> D3
    D3 -- Y --> GotoJ2([Goto J])
    D3 -- N --> ProcessBlock1
    subgraph ProcessBlock1 [ ]
        direction TB
        P1_1[If (!Class D), set CPU Opcodes bit5]
        P1_2[If TTL <= threshold from the IPMC table]
        P1_3[Then set CPU opcodes bit9]
        P1_4[If (!Class D || TTL <= threshold) {]
        P1_5[Send a copy to CPU. Go to J}]
        P1_4 --- P1_5
    end
    ProcessBlock1 --> L3S([L3-S])
    L3S --> ProcessBlock2
    subgraph ProcessBlock2 [ ]
        direction TB
        P2_1[Port_Match = TRUE]
        P2_2[If (SP_Check) {]
        P2_3[Port_Match=FALSE]
        P2_4[If (VID of the packet != VID from the Table) Port_Match = FALSE]
        P2_5[else {]
        P2_6[If (T bit of Source port == TS) {]
        P2_7[If (T bit of the Source port) {]
        P2_8[If (TGID of the Source port == port_TGID from IPMC table) Port_Match=TRUE}]
        P2_9[Else if (Source port == port_TGID from IPMC Table) Port_Match =TRUE}]
        P2_10[}]
        P2_11[}]
        P2_12[}]
    end
    ProcessBlock2 --> D4{Port_Match?}
    D4 -- N --> ProcessBlock3
    subgraph ProcessBlock3 [ ]
        direction TB
        P3_1[If (CD) send a copy to CPU]
        P3_2[Goto J]
    end
    ProcessBlock3 --> GotoJ3([Goto J])
    D4 -- Y --> ProcessBlock4
    subgraph ProcessBlock4 [ ]
        direction TB
        P4_1[Get L3 PortBitmap from the IPMC Table]
        P4_2[Increment MltRoutPkt counter for the port (if the corresponding L3 PortBitmap is not zero)]
        P4_3[PortBitmap = PortBitmap | L3 PortBitmap]
        P4_4[Goto J]
    end
    ProcessBlock4 --> GotoJ3

```

Fig. 18

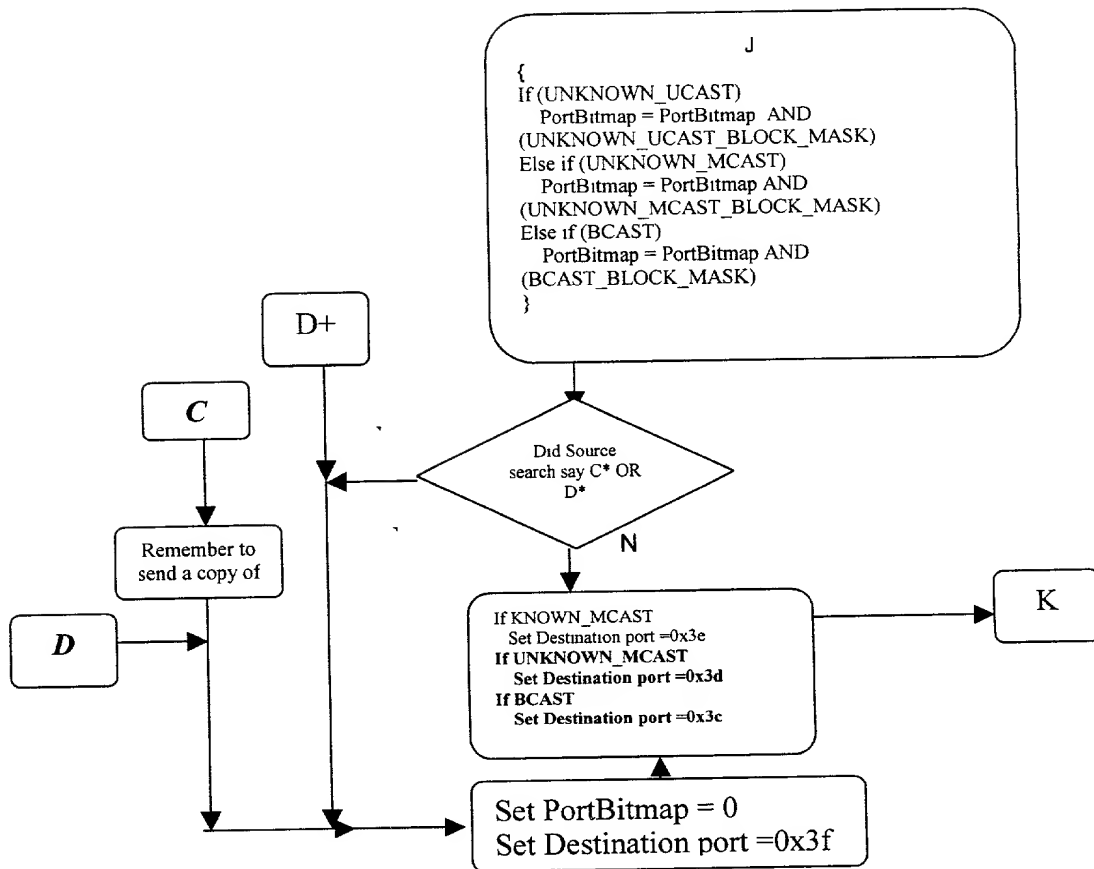


Fig. 19

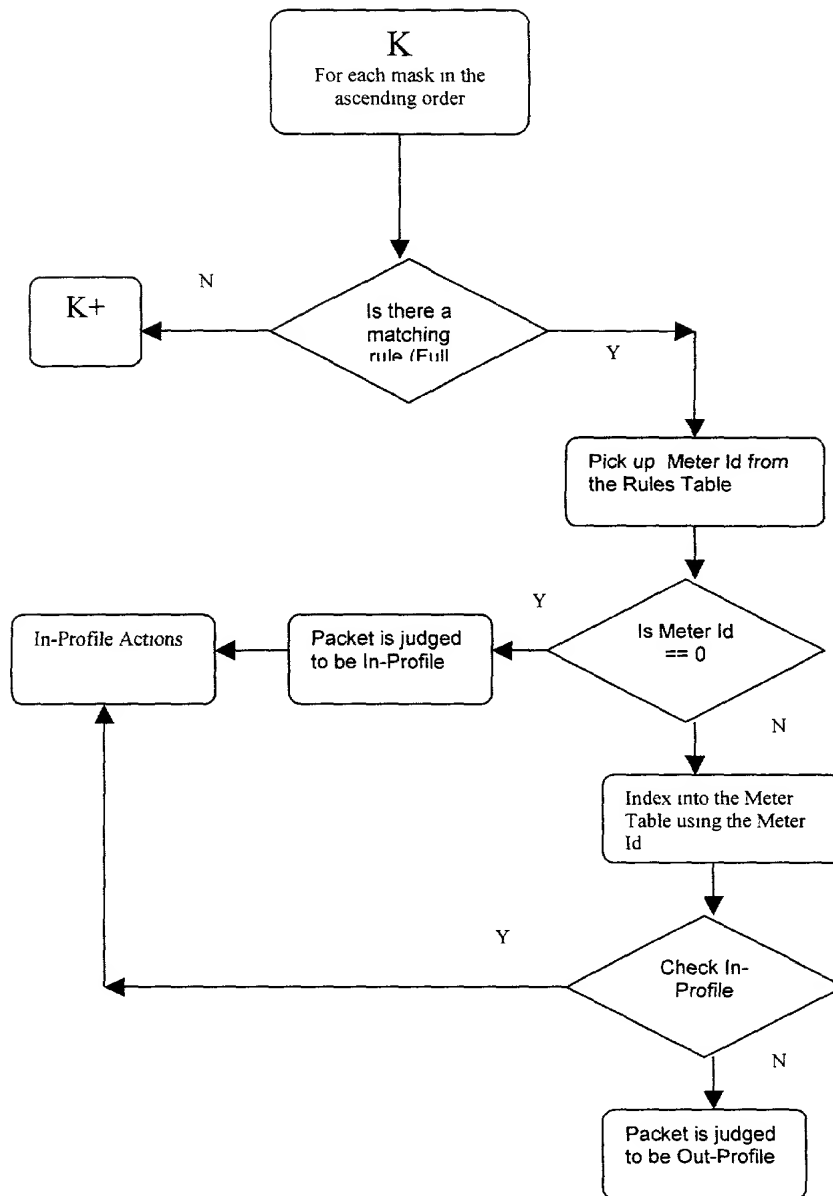


Fig. 20

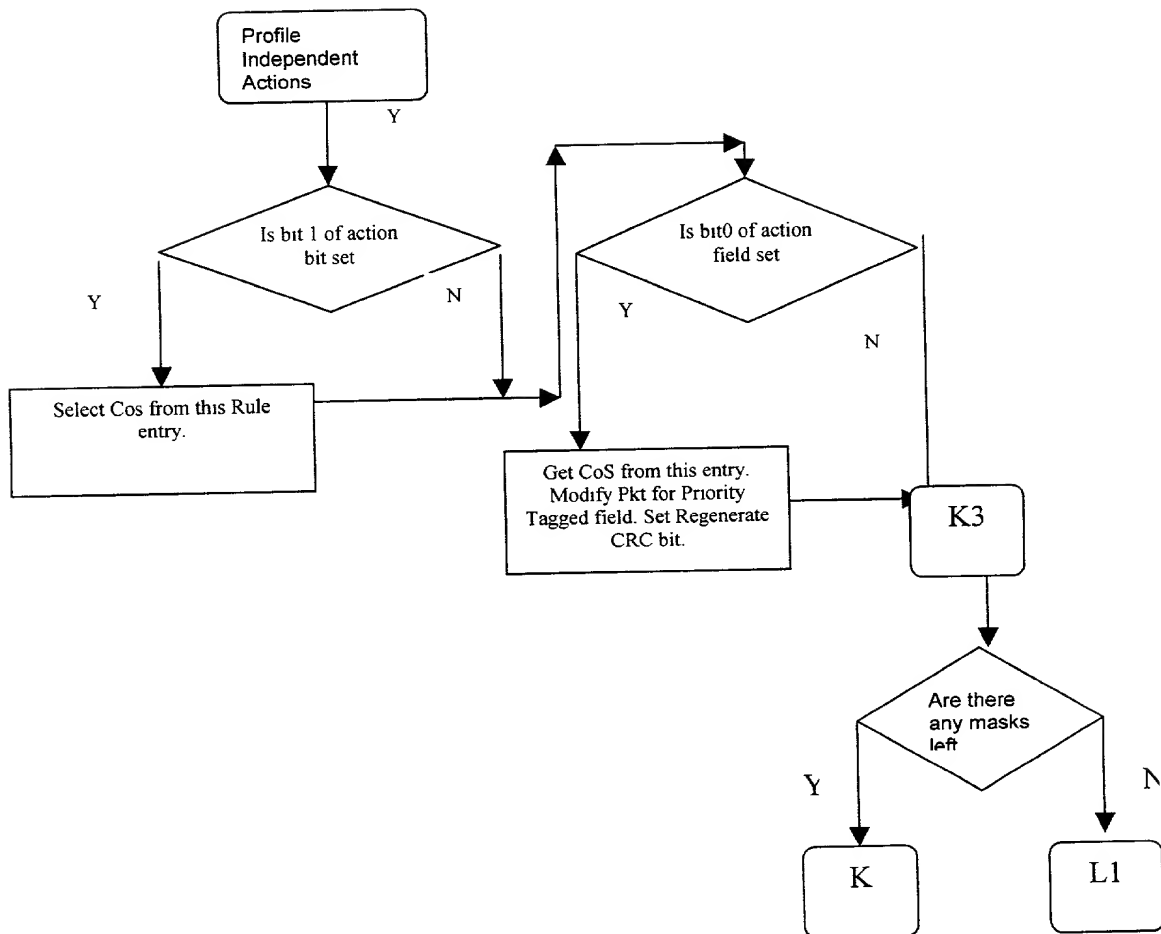


Fig. 21

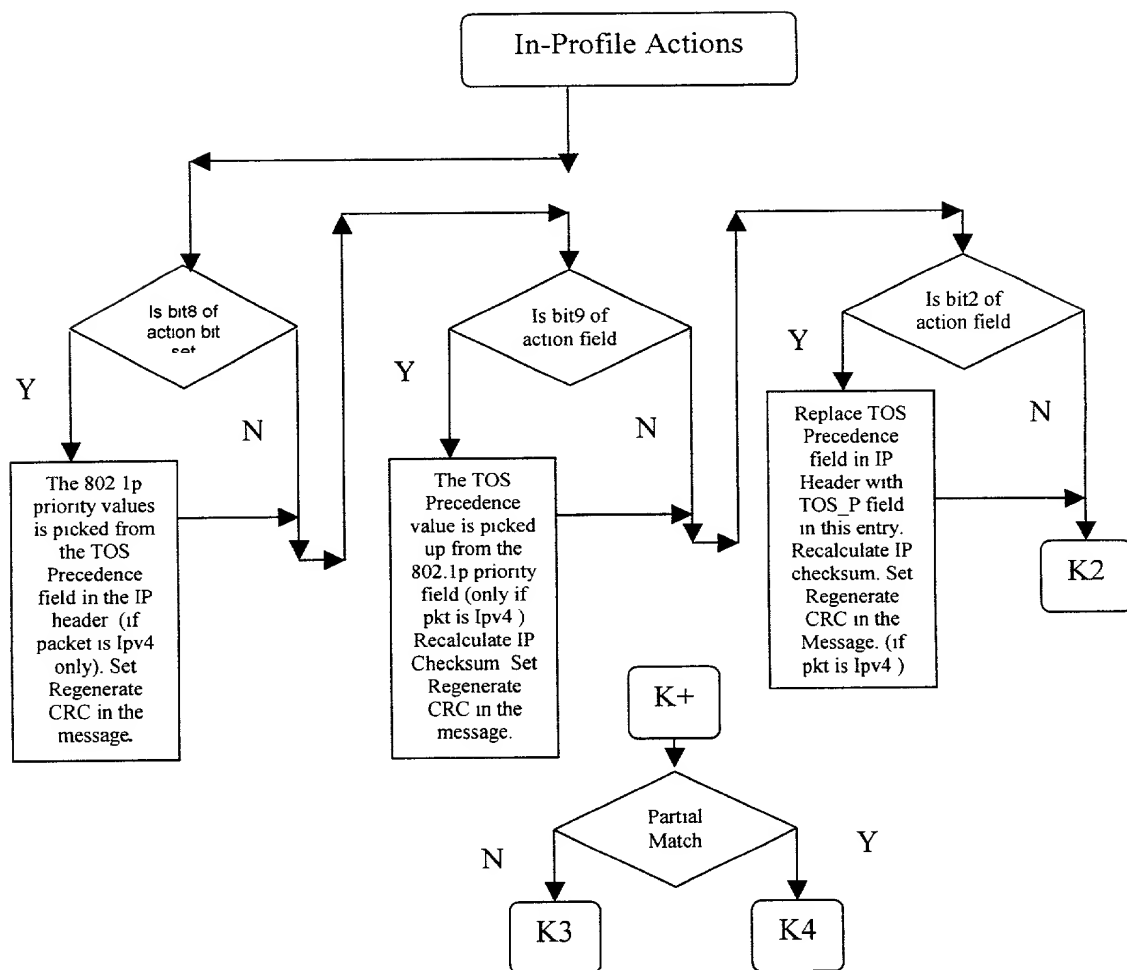


Fig. 22

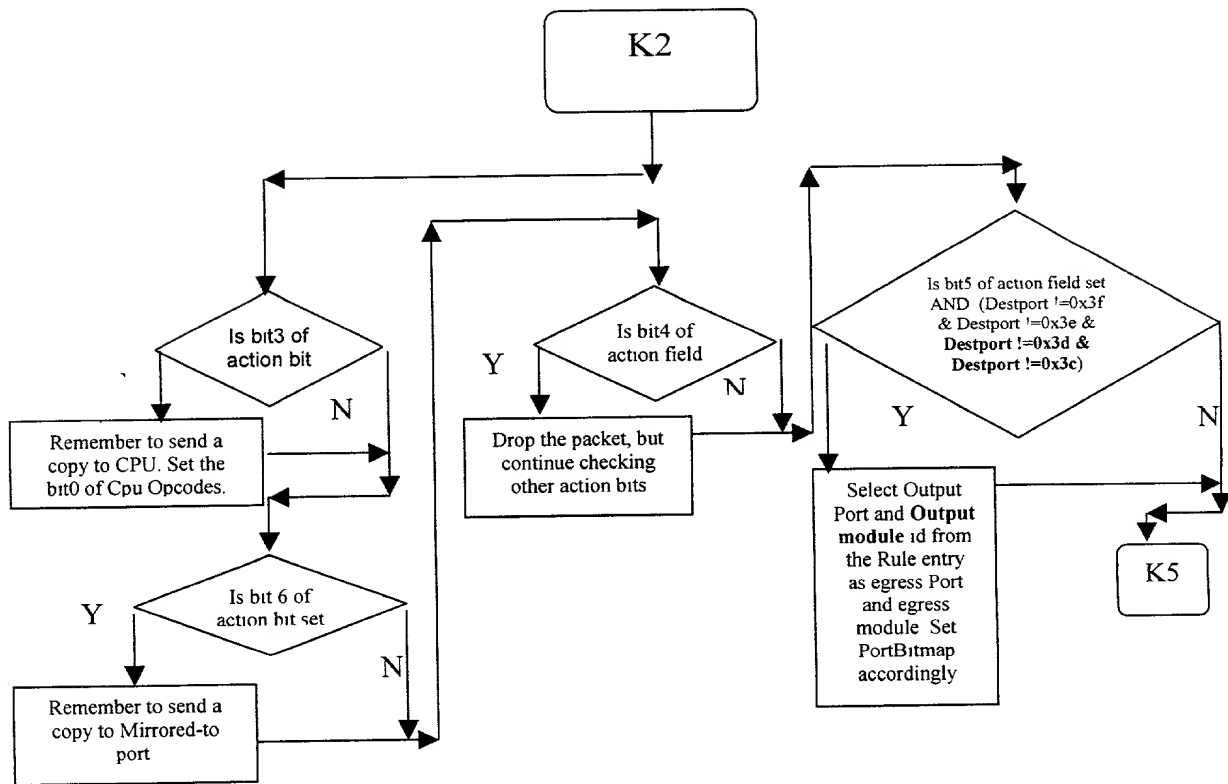


Fig. 23

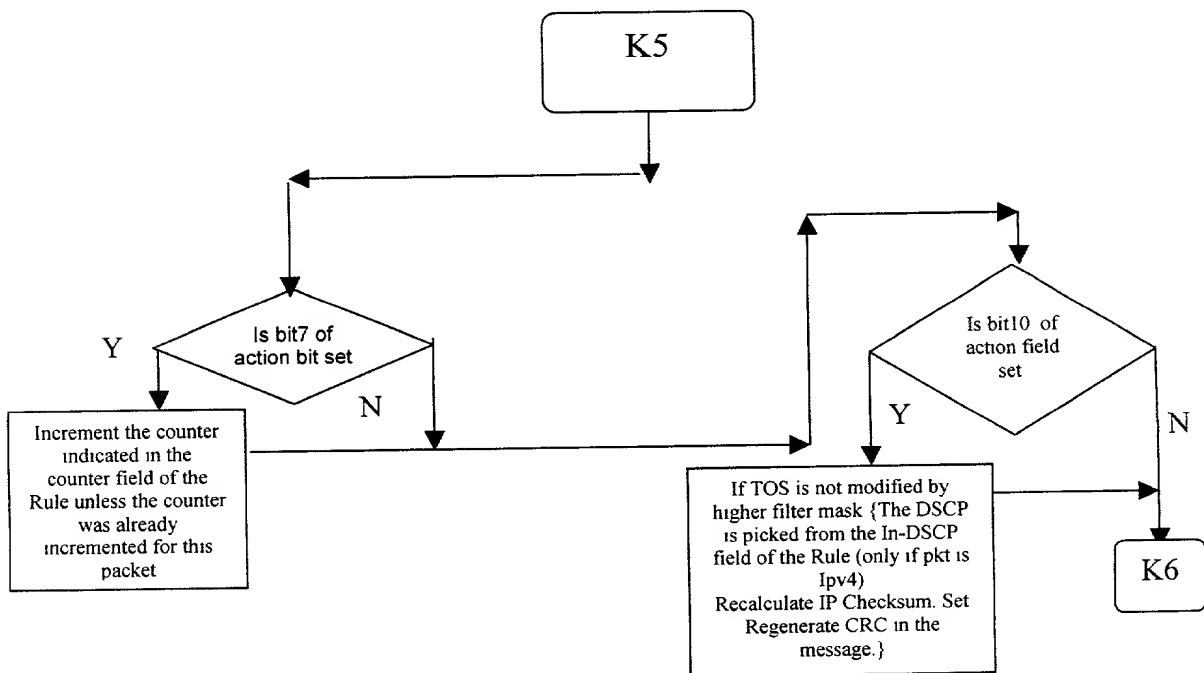


Fig. 24

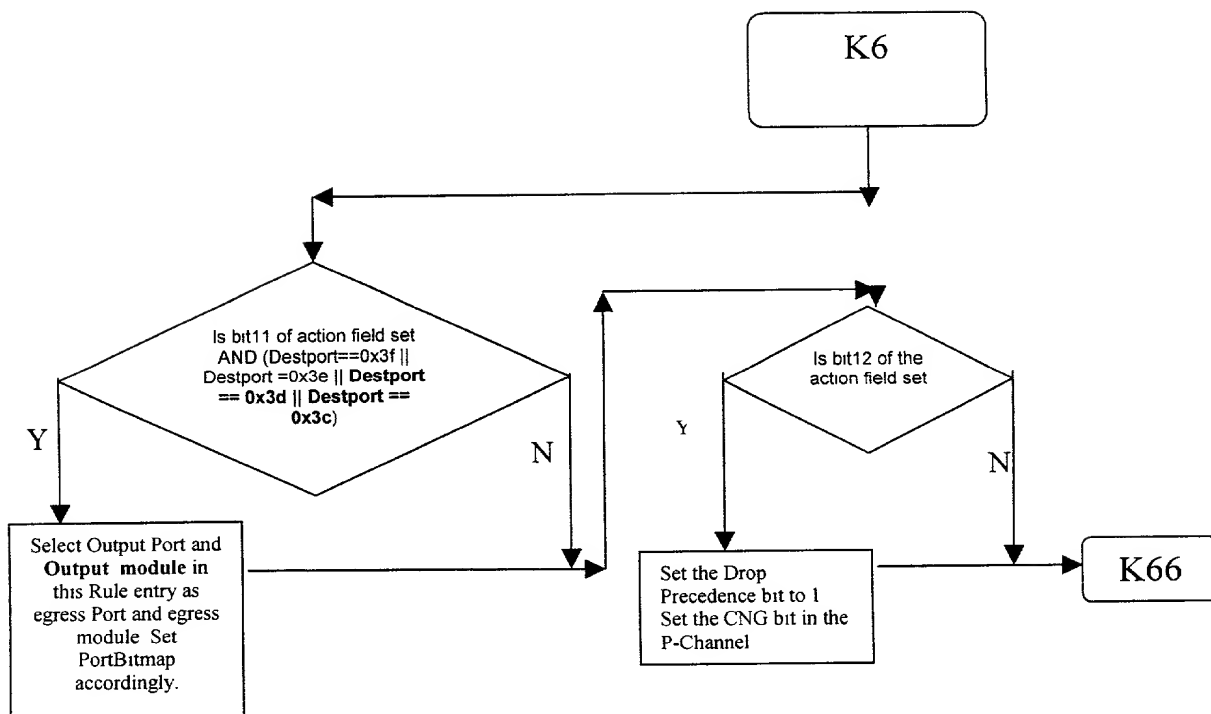


Fig. 25

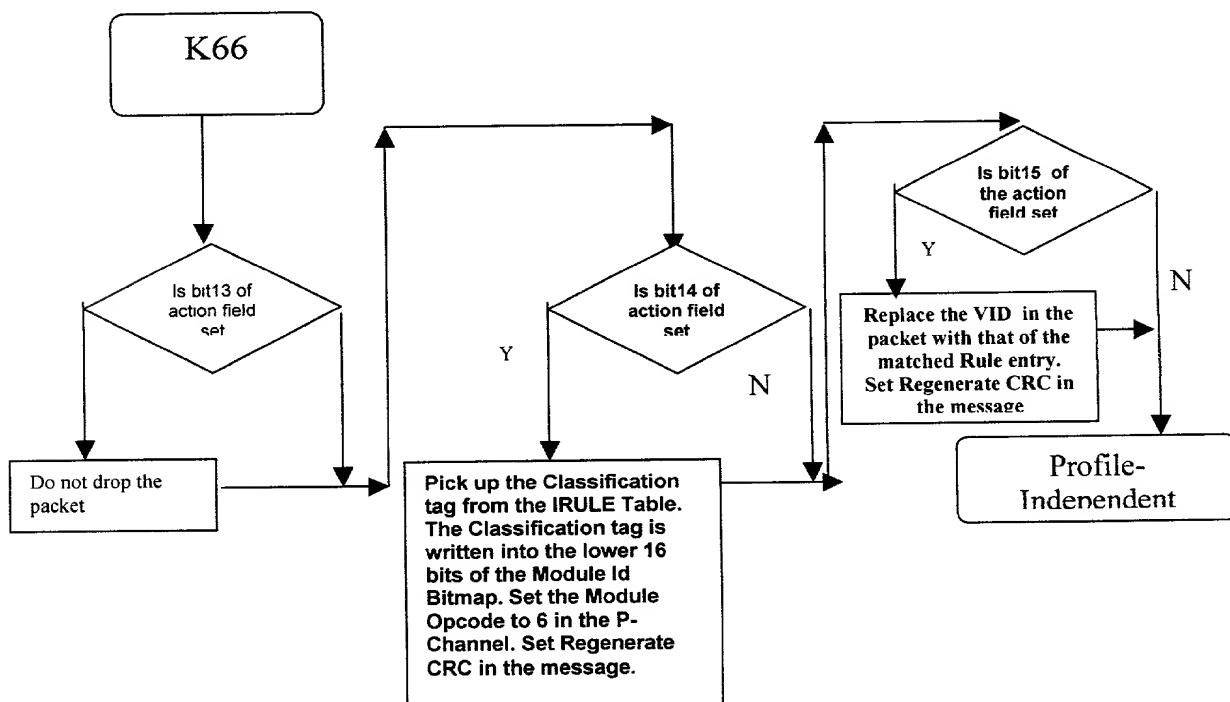


Fig. 26

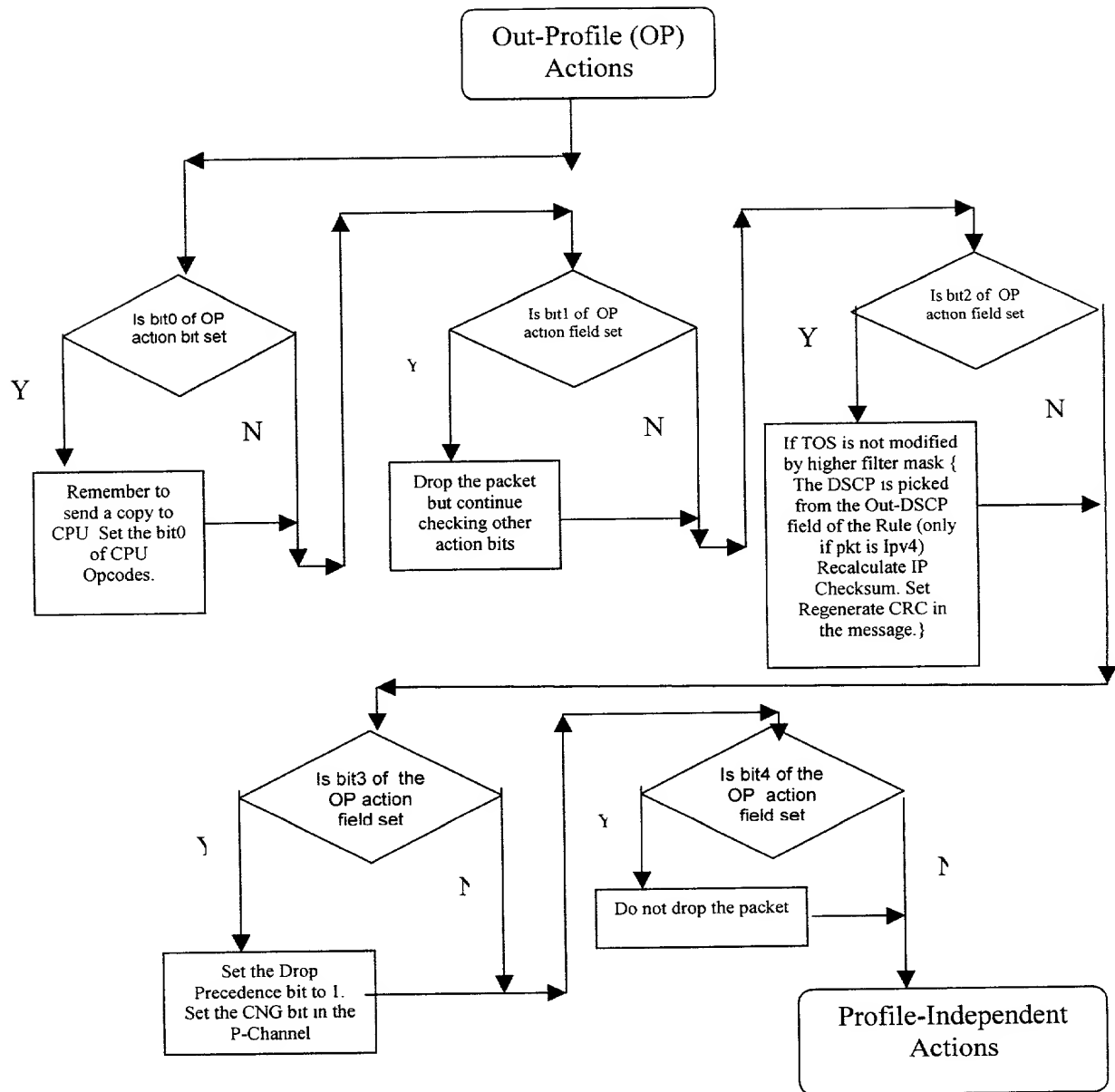


Fig. 27



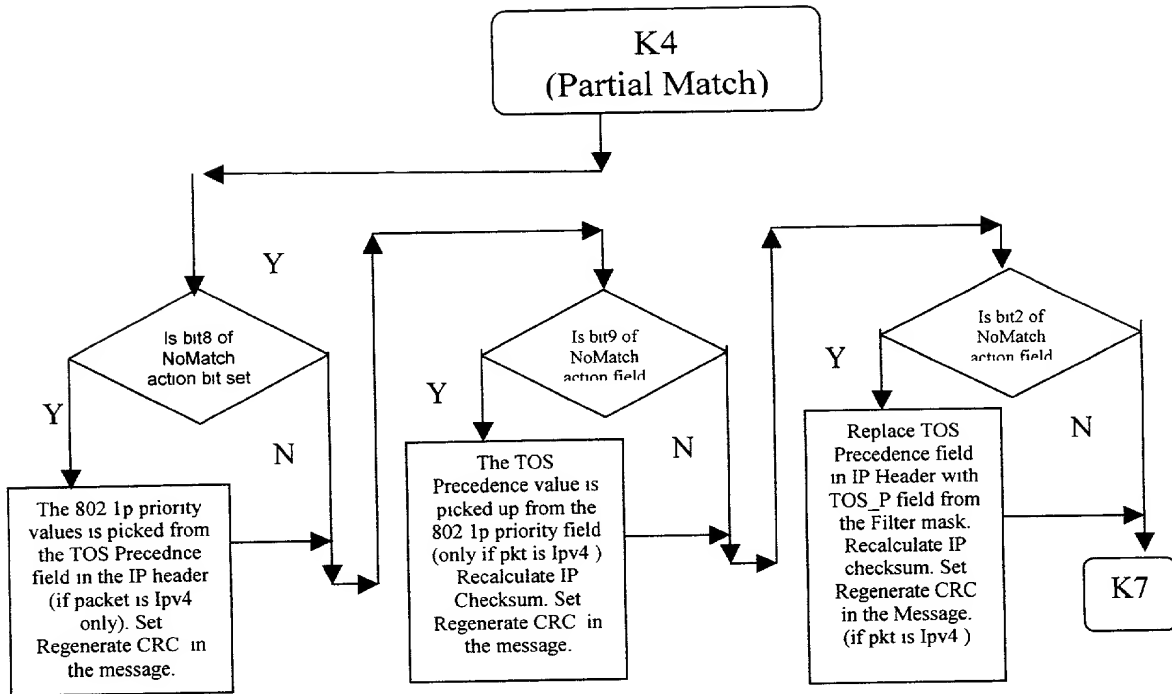


Fig. 28

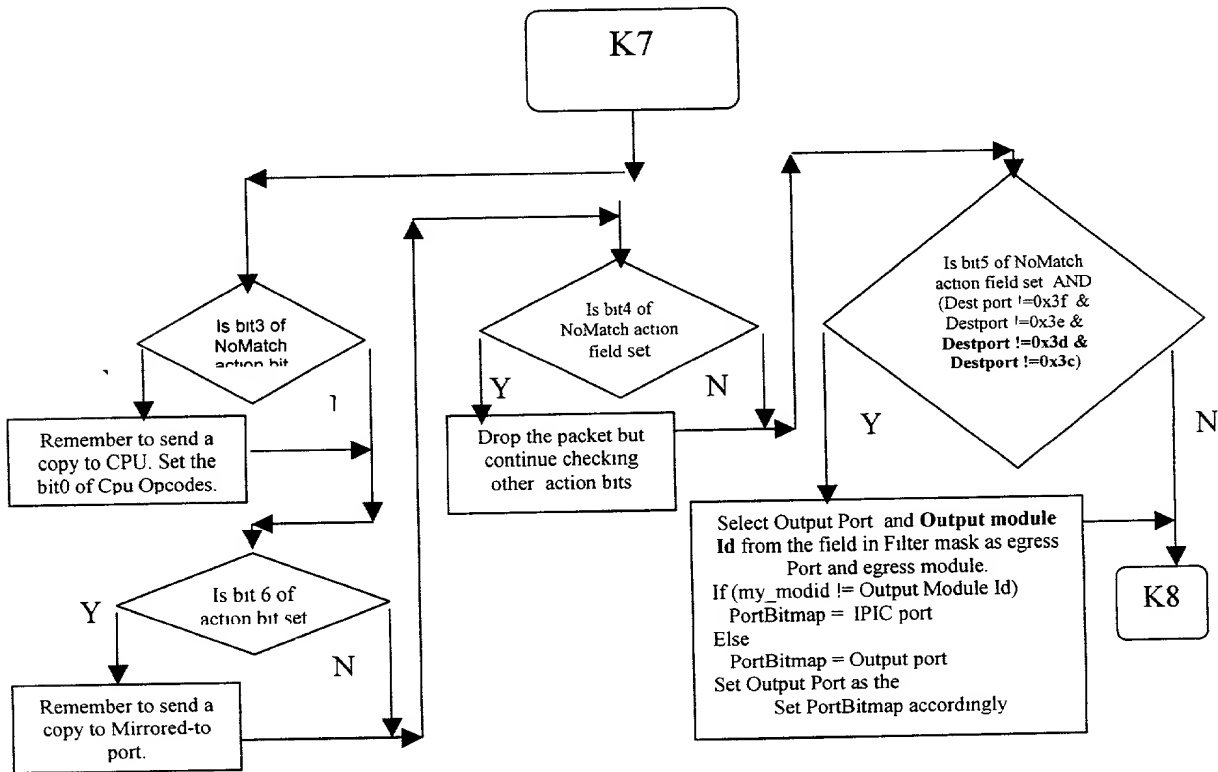


Fig. 29

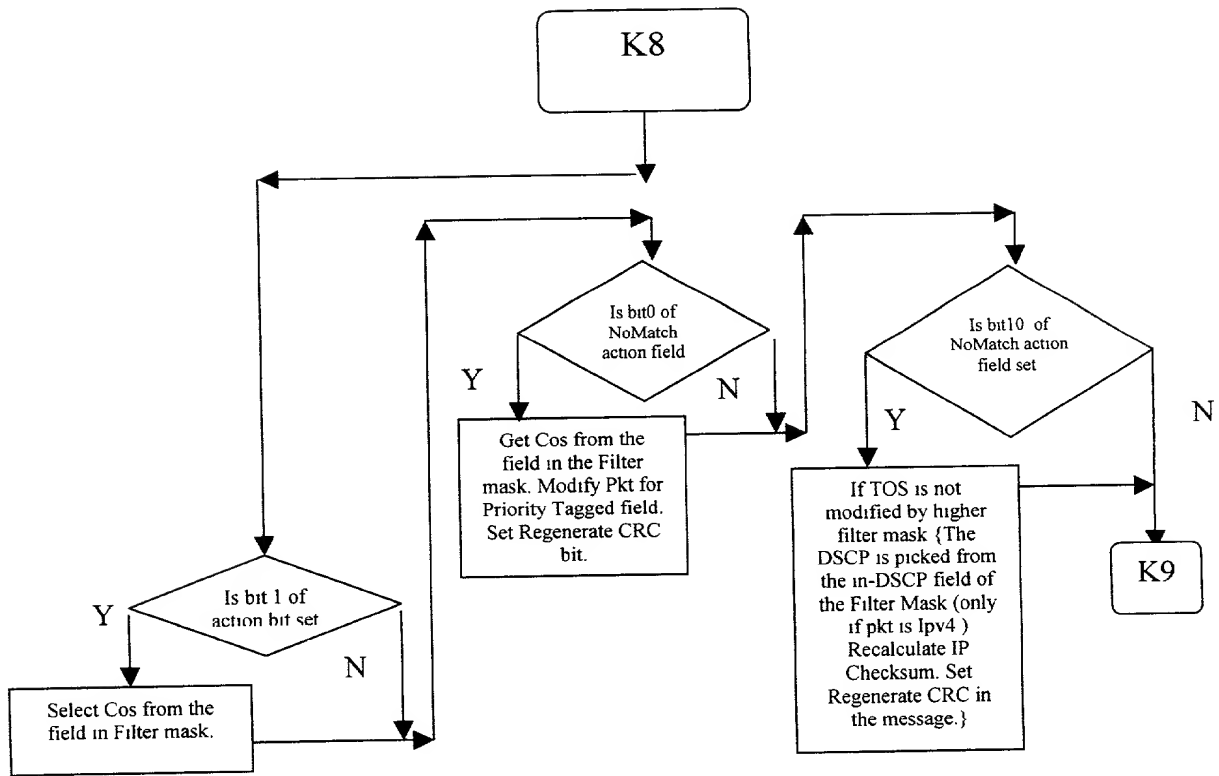


Fig. 30

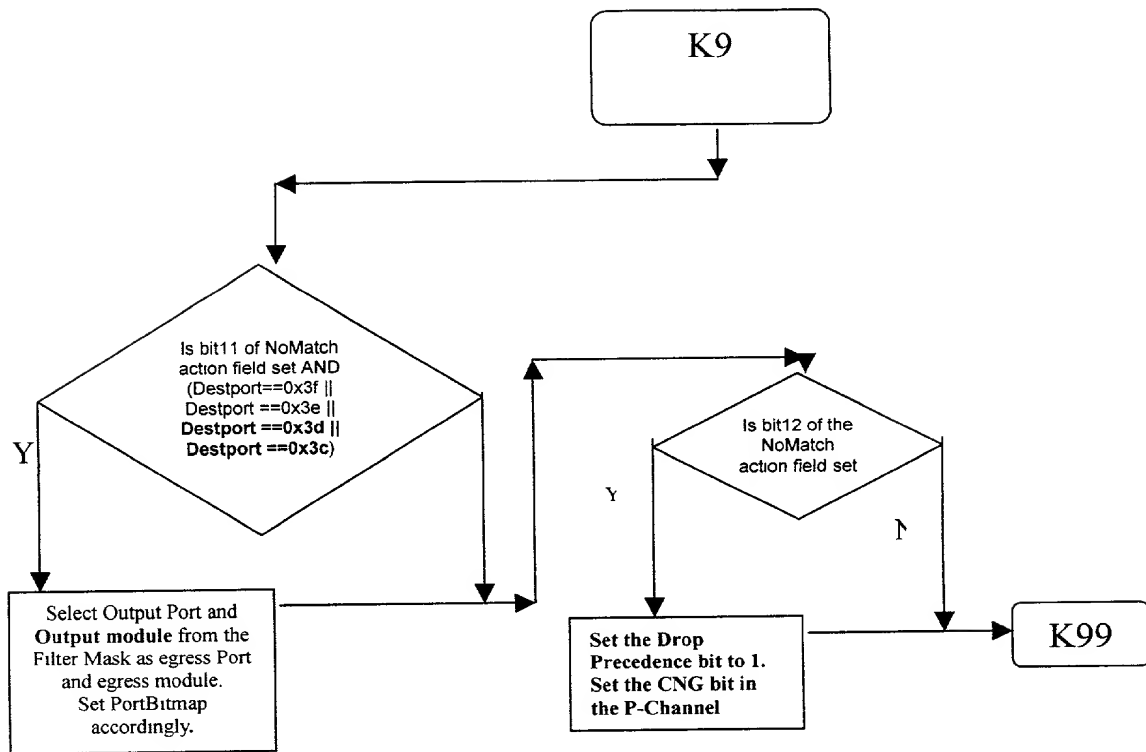


Fig. 31

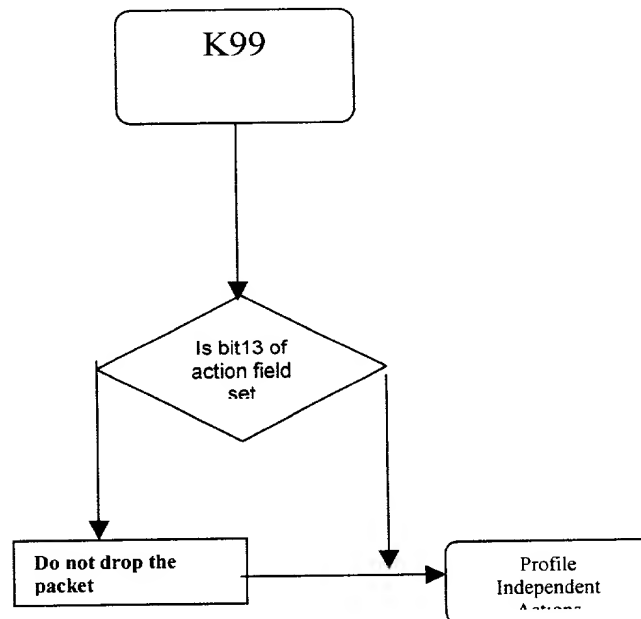


Fig. 32

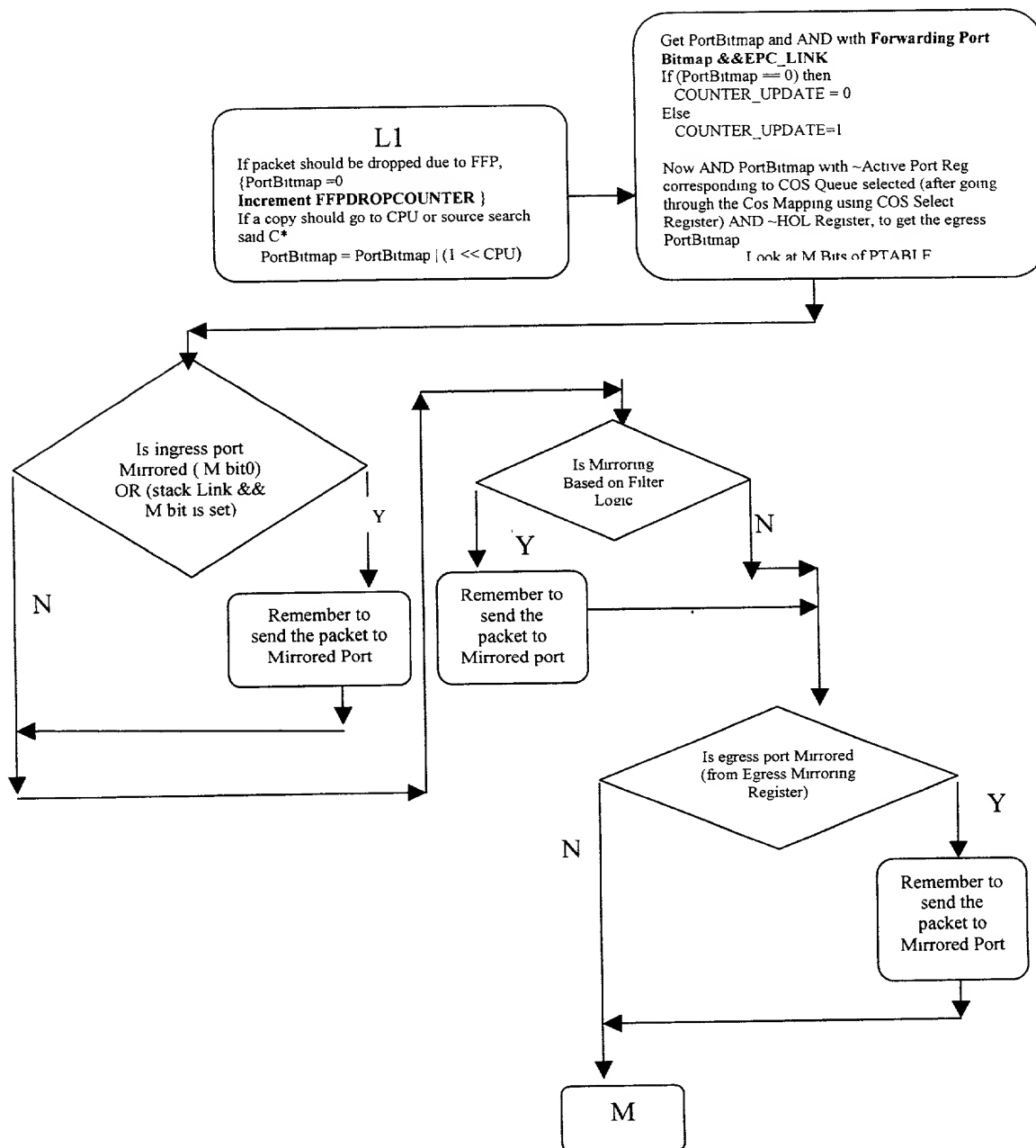


Fig. 33

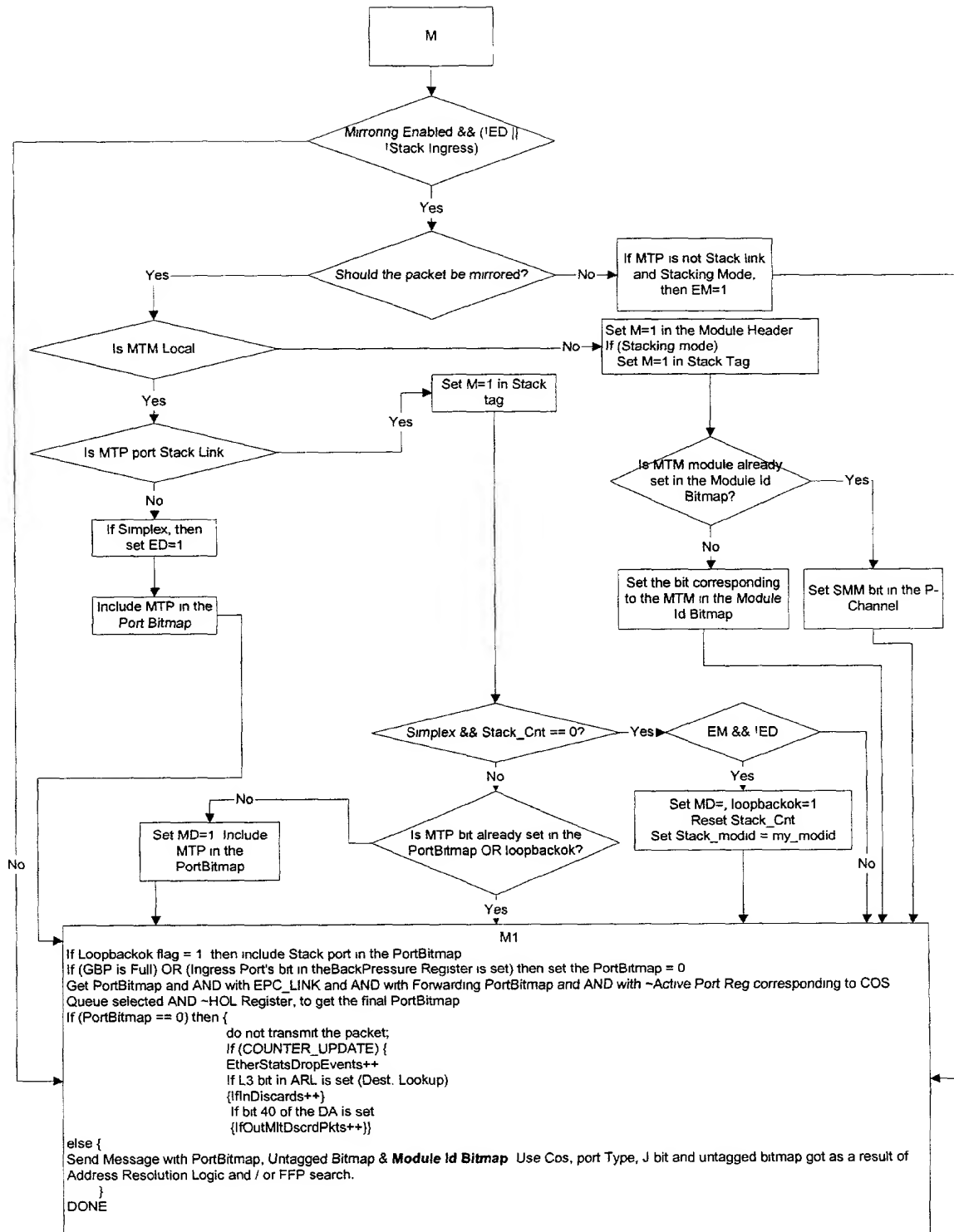


Fig. 34

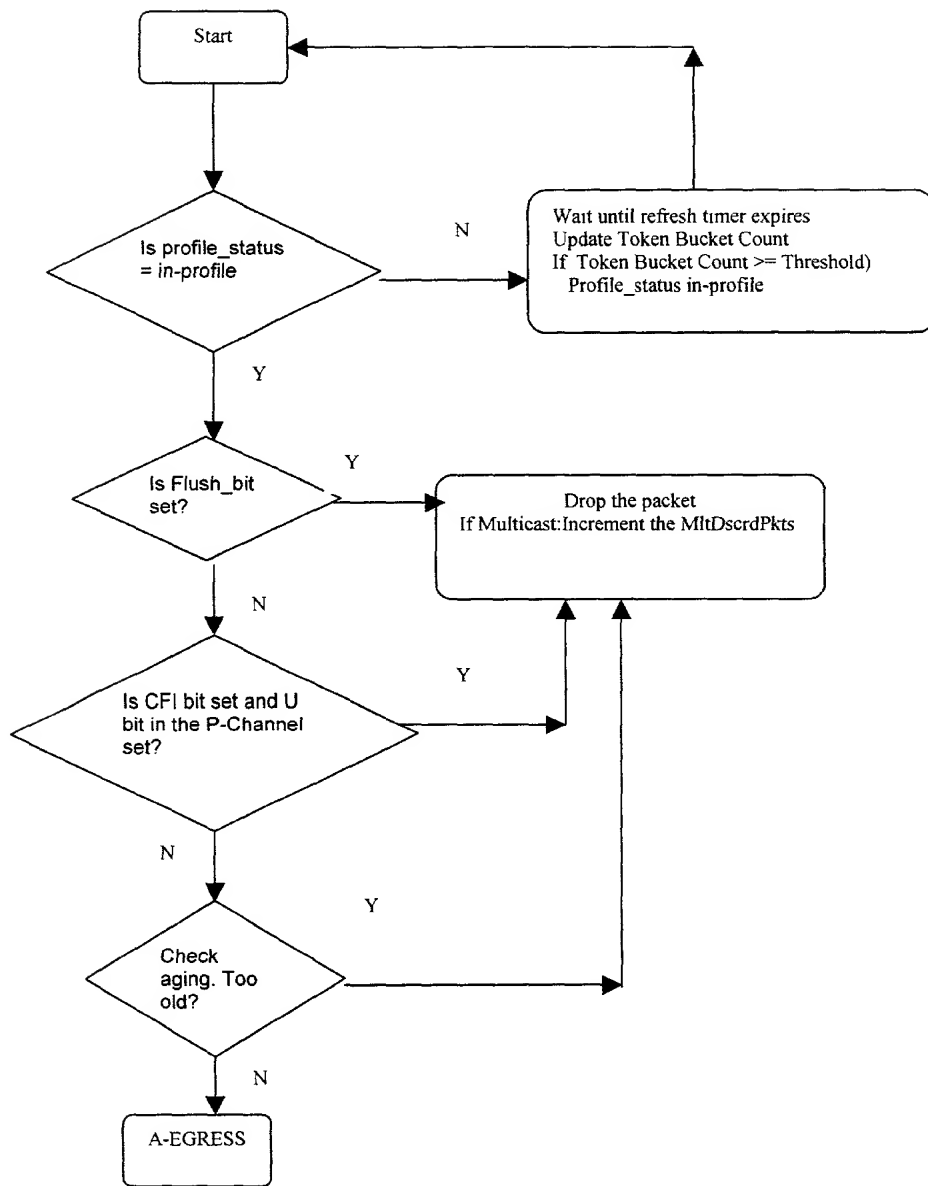


Fig. 35

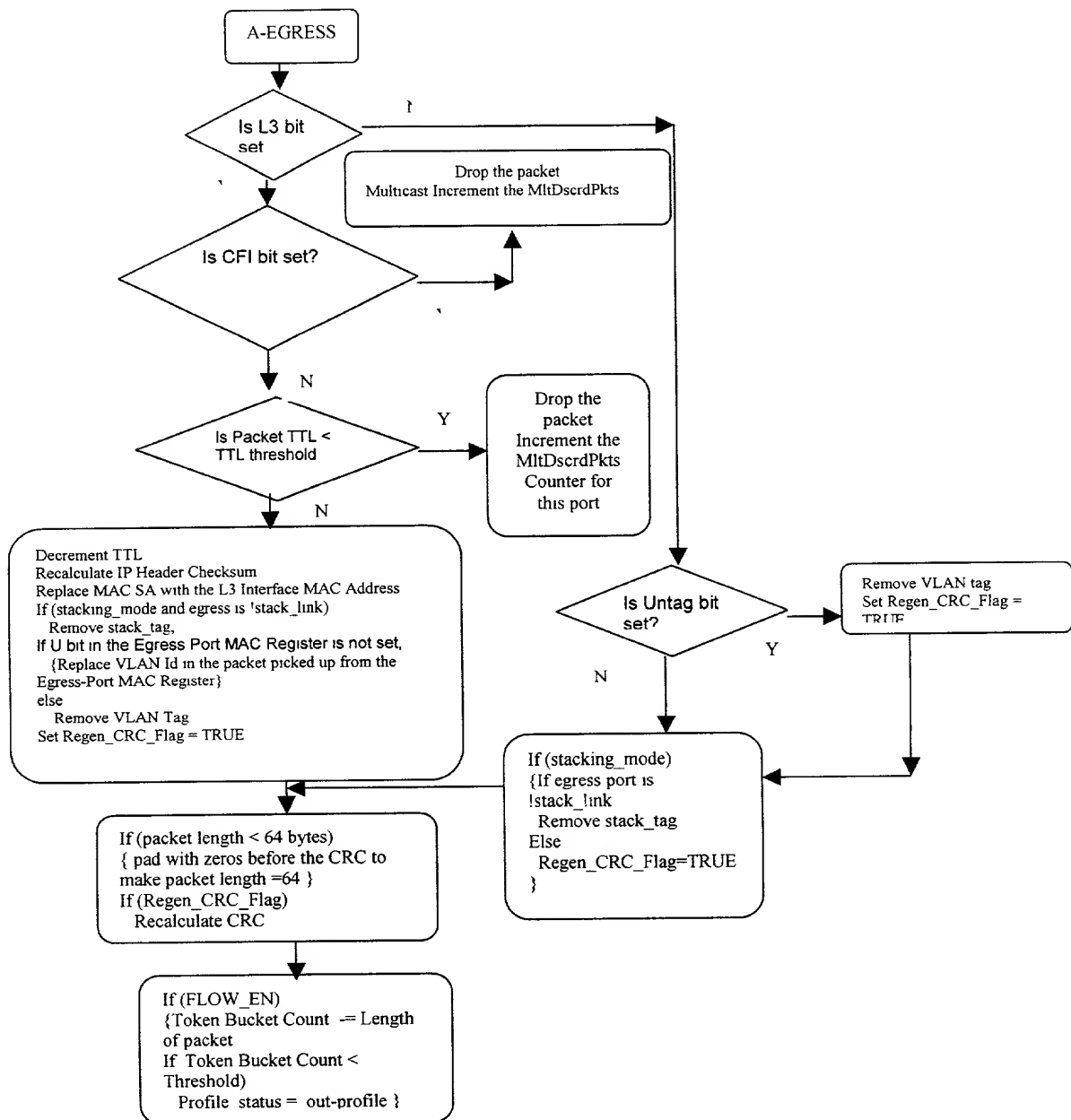


Fig. 36

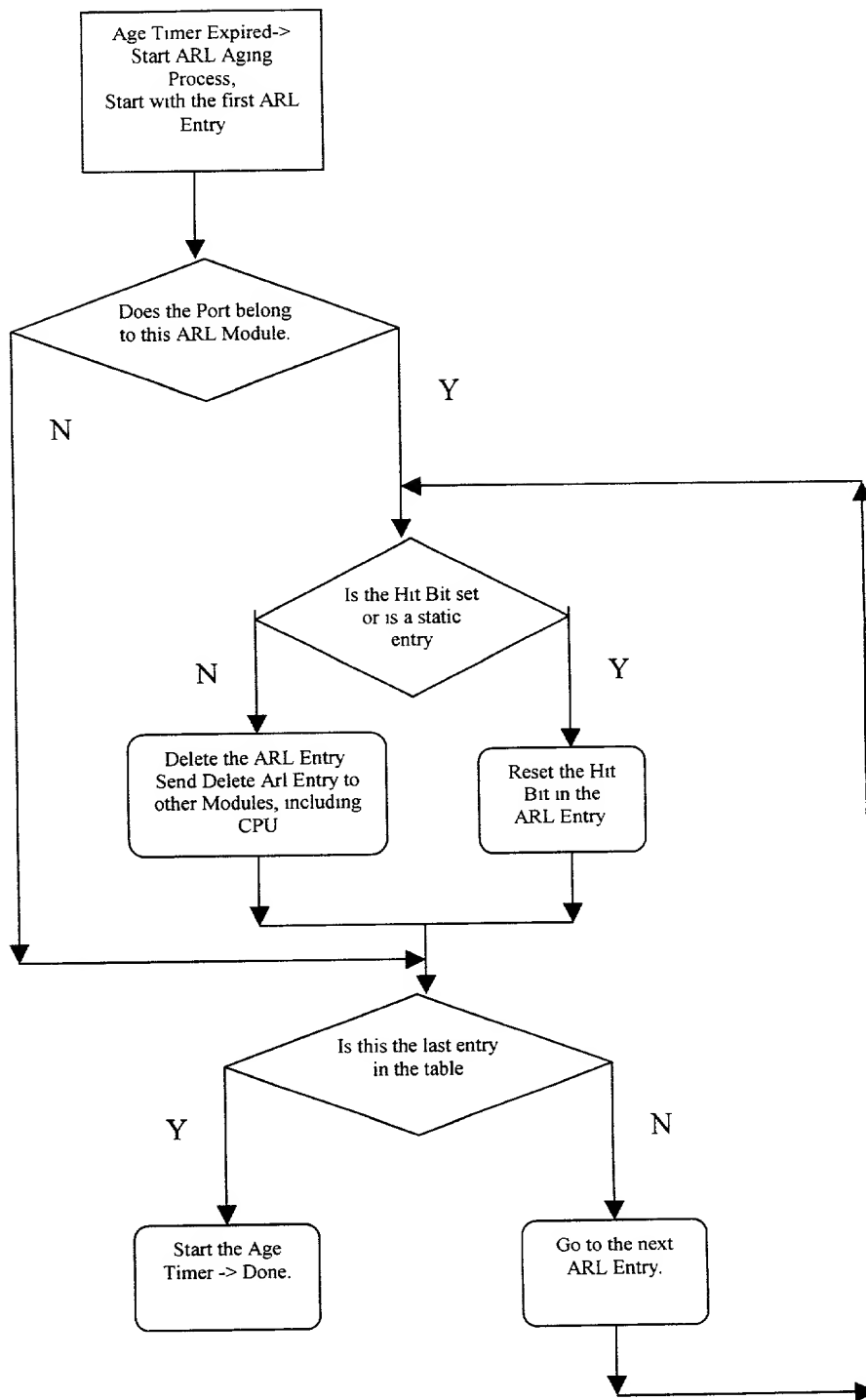


Fig. 37



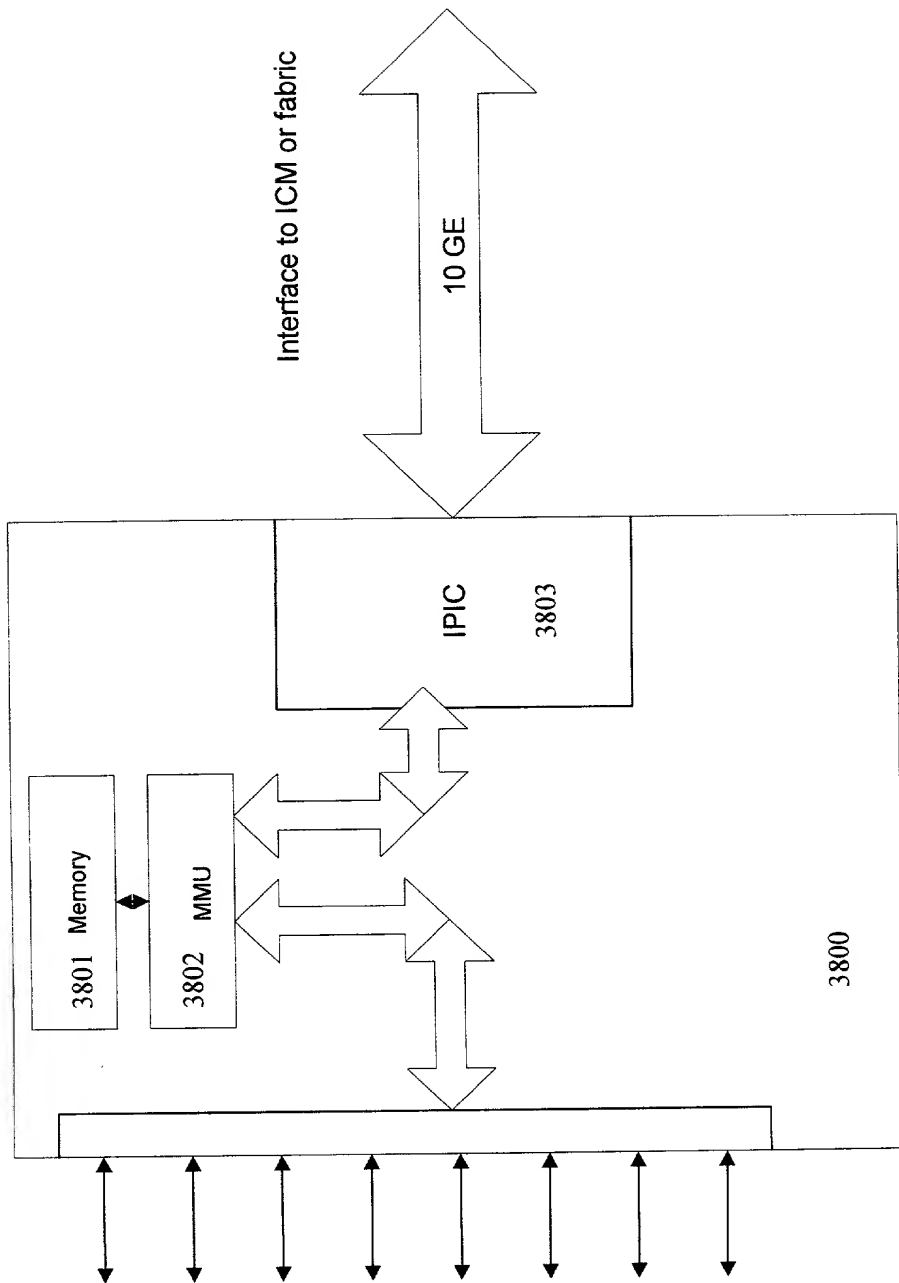


Fig. 38

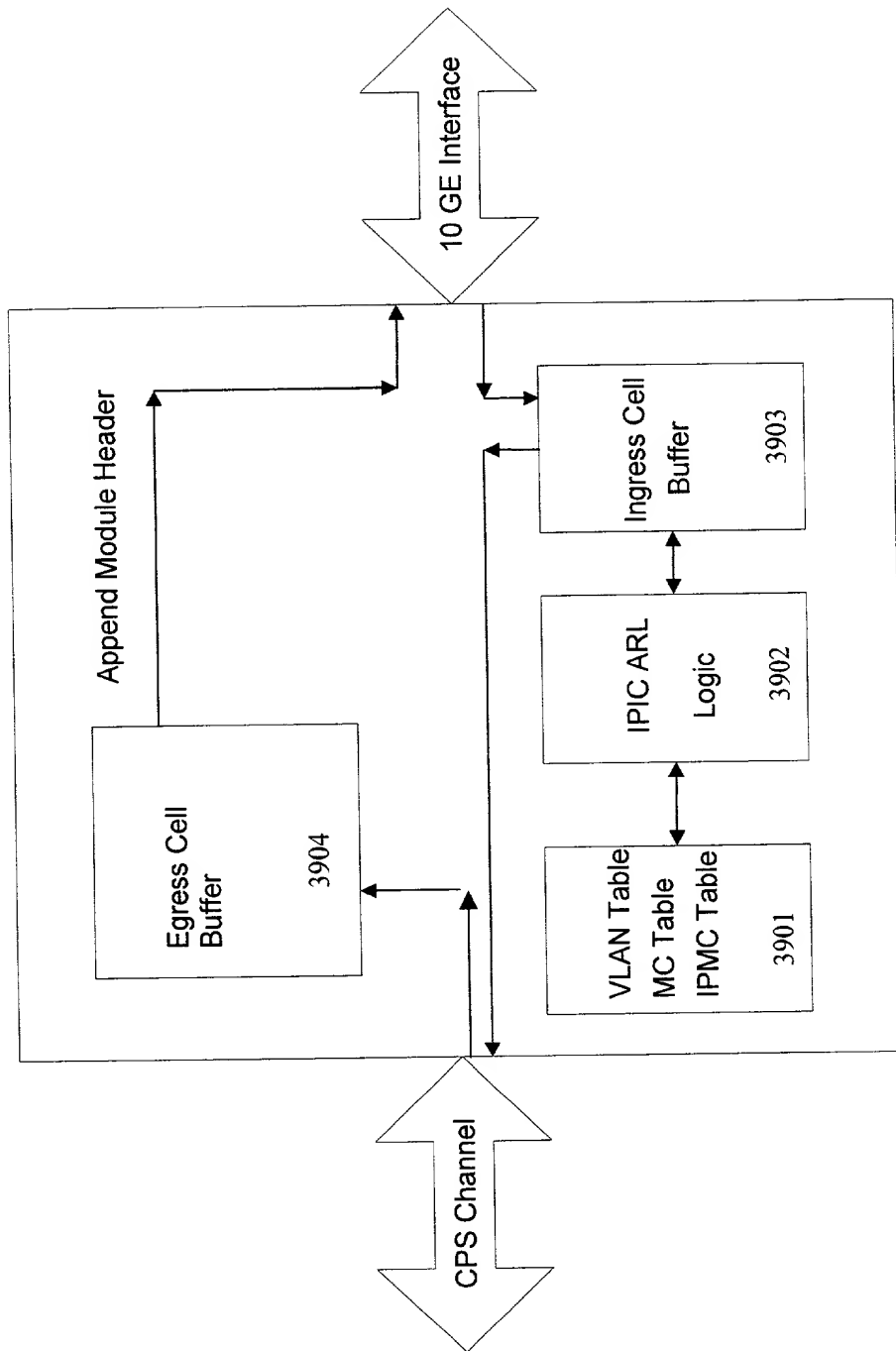


Fig. 39

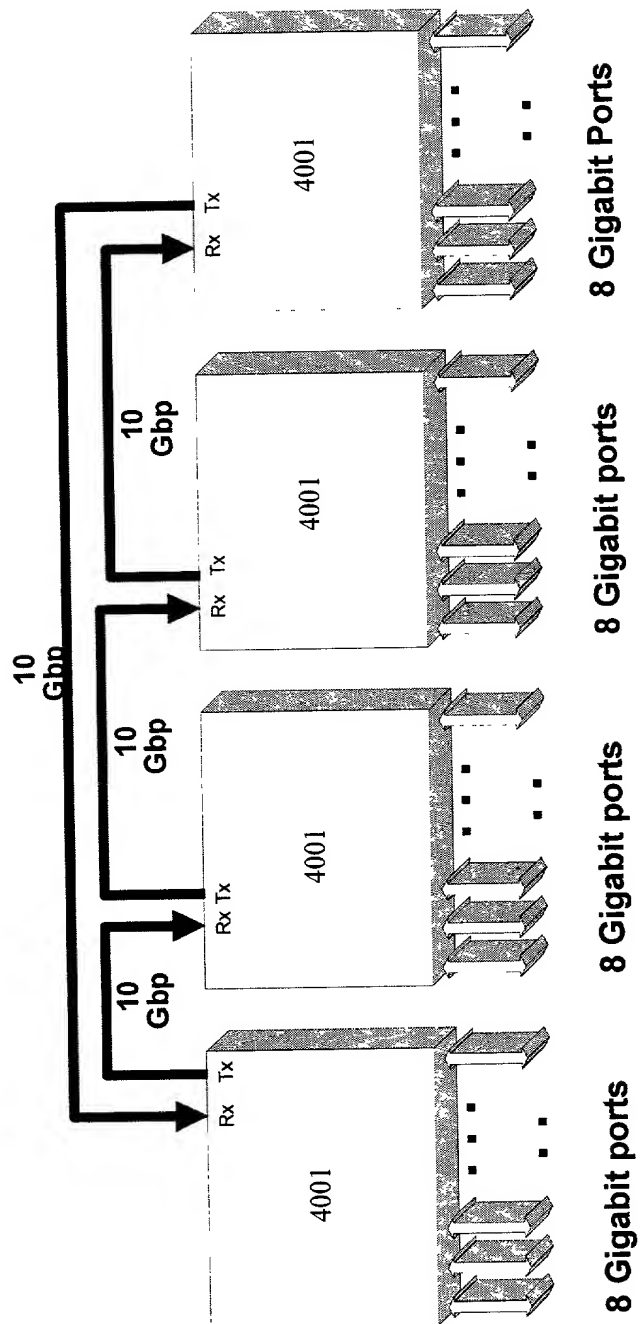


Fig. 40

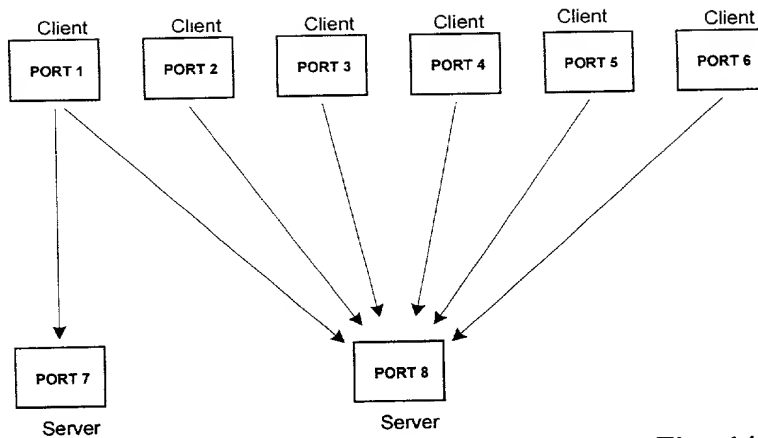


Fig. 41

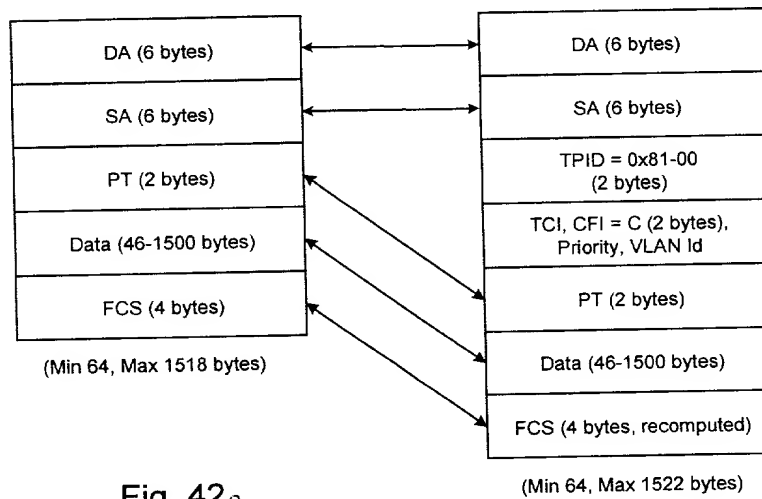


Fig. 42 a

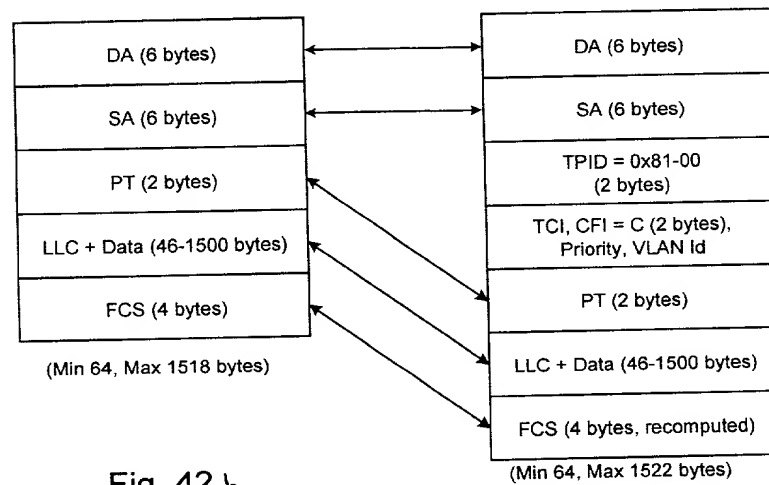


Fig. 42 b

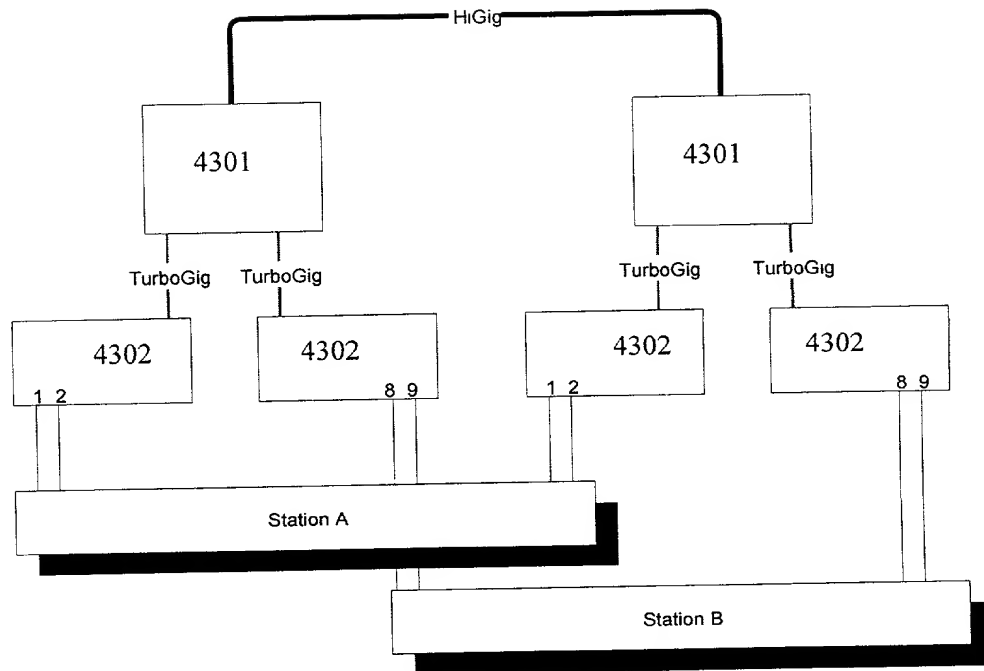


Fig. 43

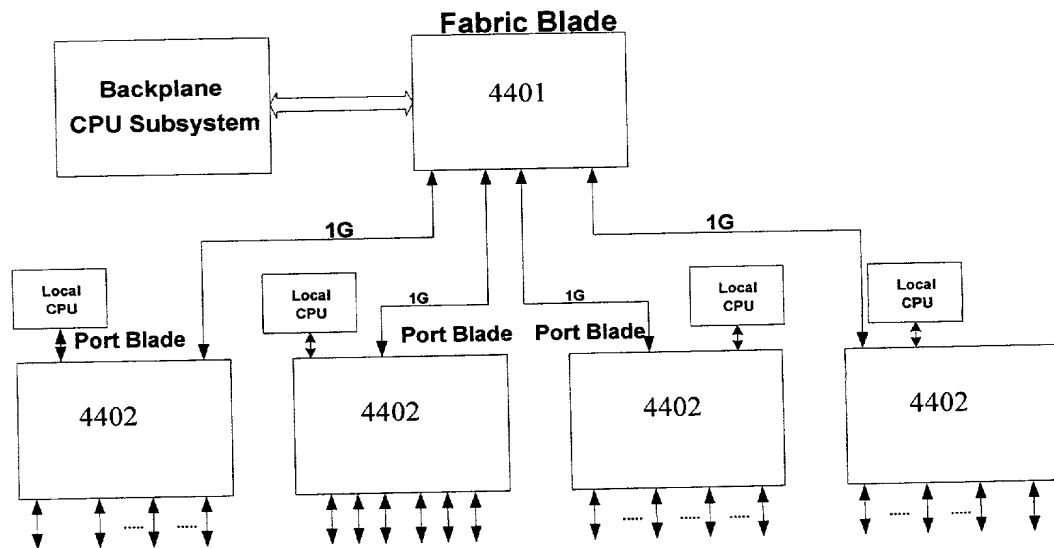


Fig. 44

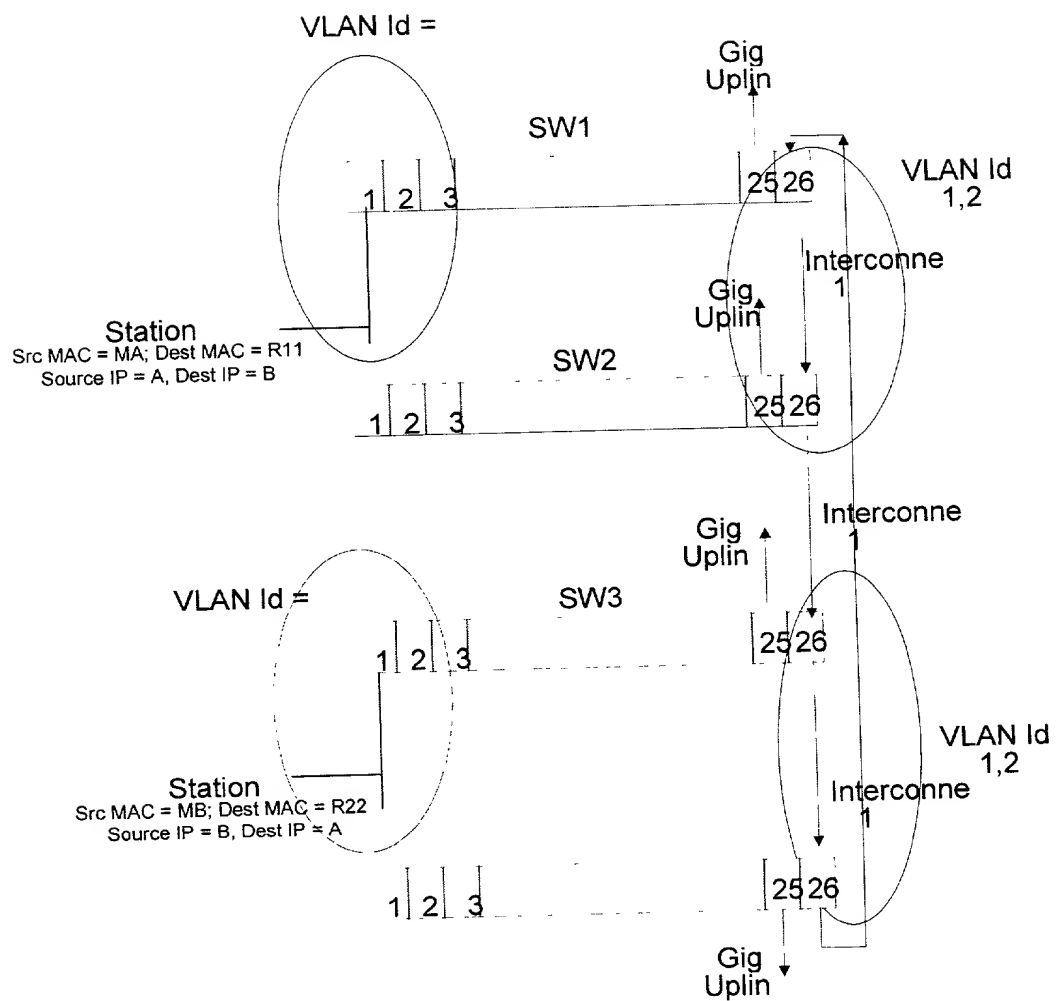


Fig. 45

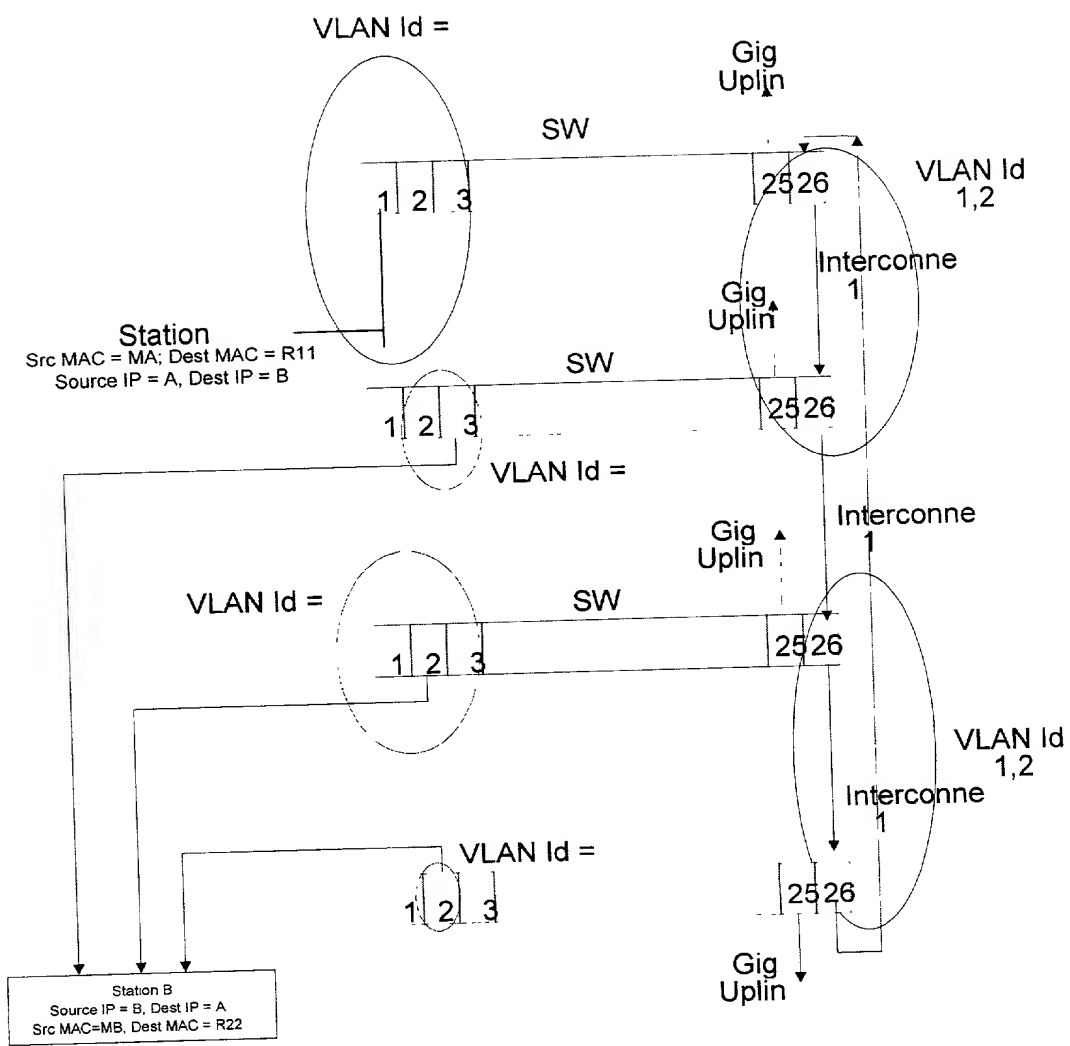


Fig. 46